

LAYER 1 : TOP
LAYER 2 : SGND1
LAYER 3 : IN1
LAYER 4 : IN2
LAYER 5 : VCC
LAYER 6 : IN3
LAYER 7 : SGND2
LAYER 8 : BOT

Cable Docking

TV_OUT
VGA
RJ-45
CIR/Pwr btn
SPDIF Out
Stereo MIC
Headphone Jack
USB Port
VOL Cntr

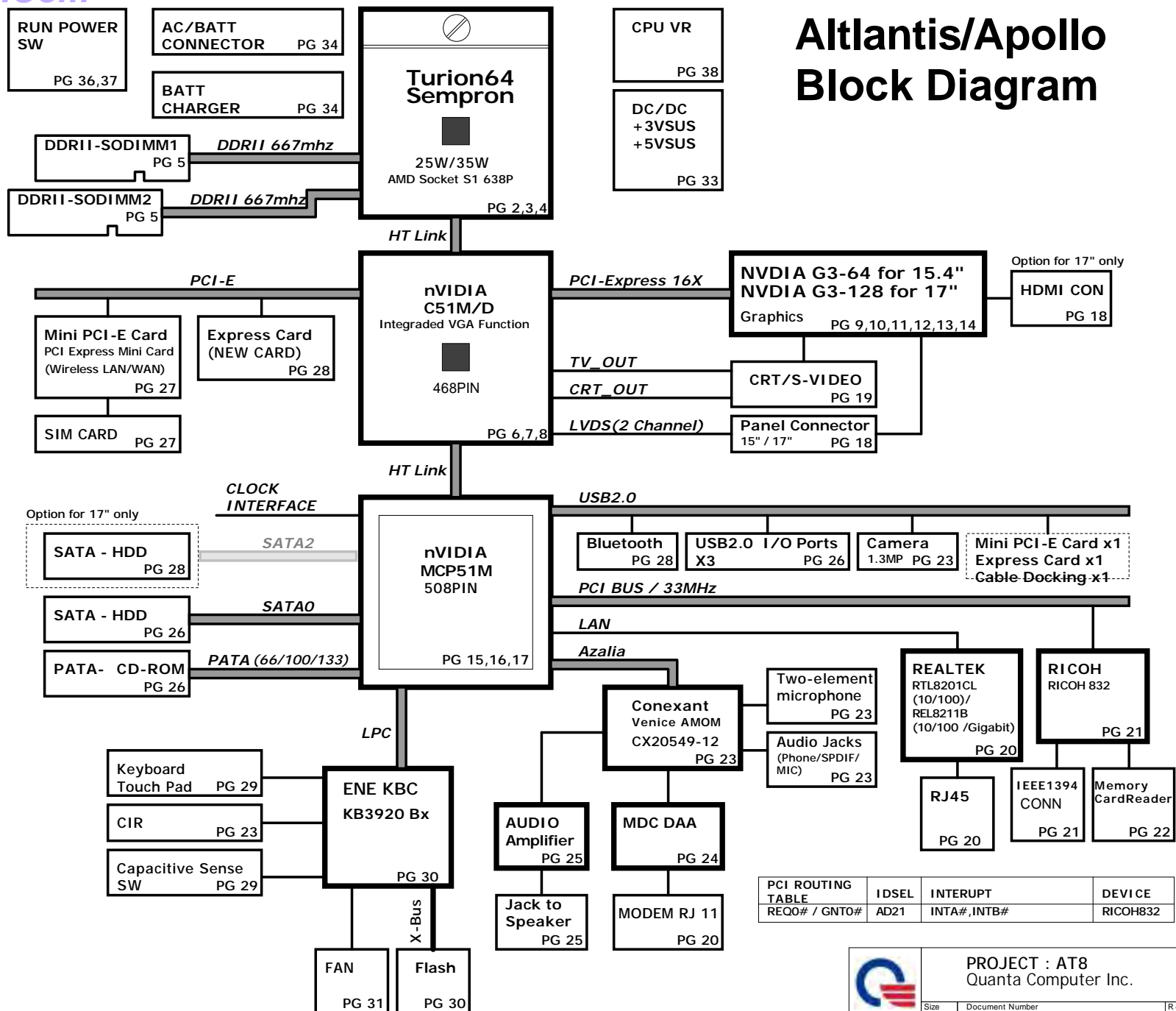
PG 31

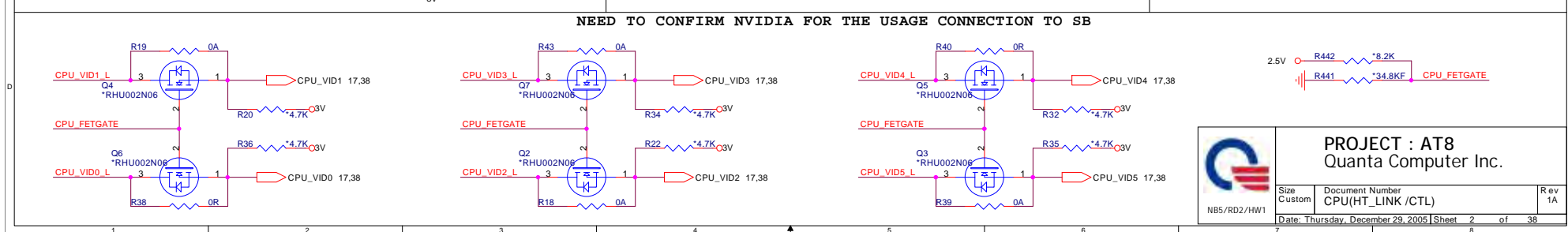
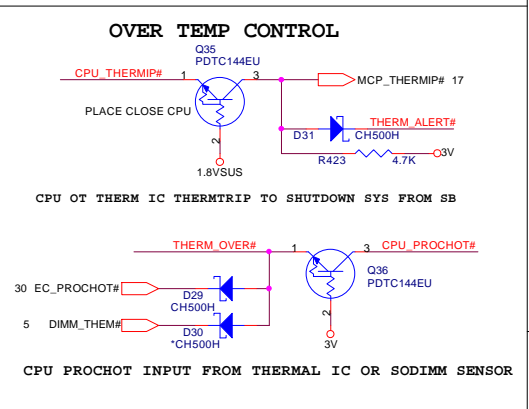
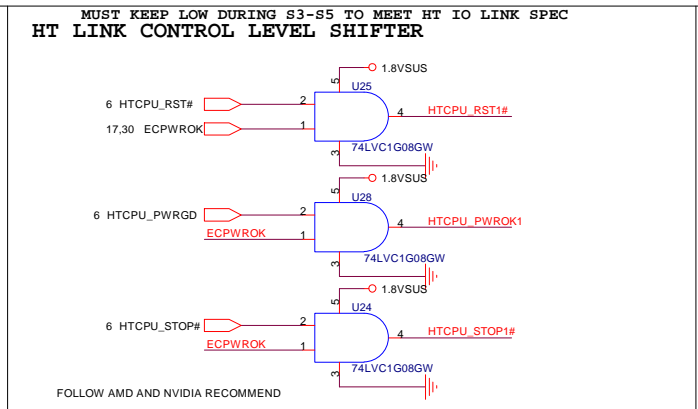
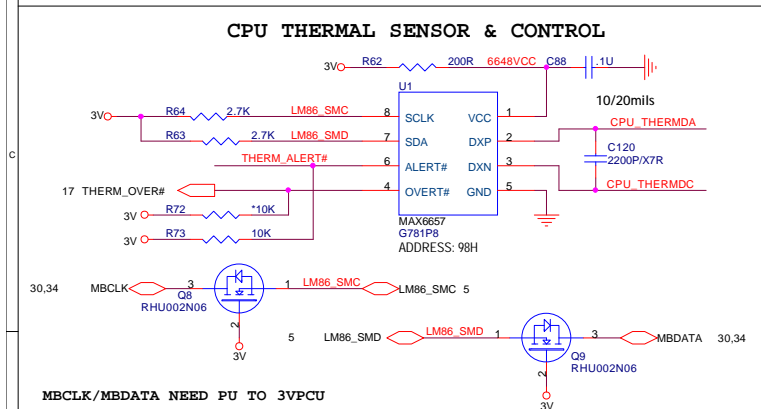
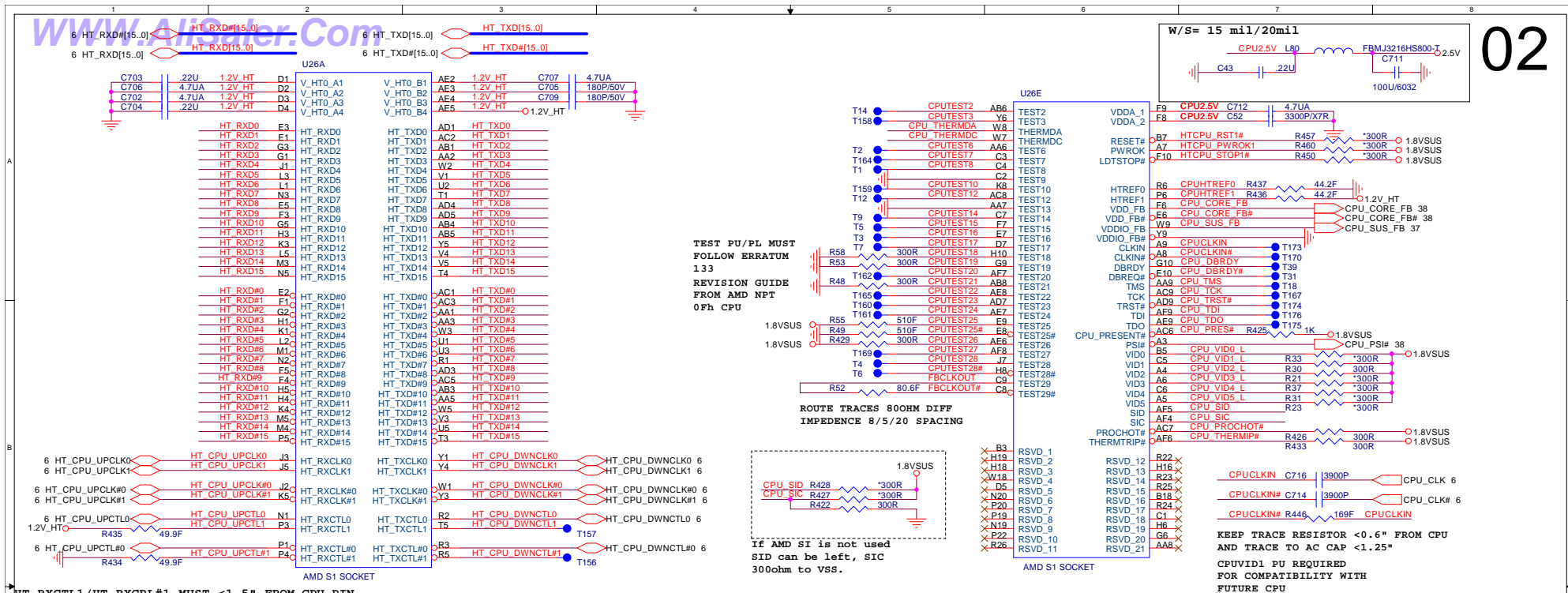
VAULE DEFINE

A=0603,B=0805,C=1206,F=1%,
OTHER IS 0402

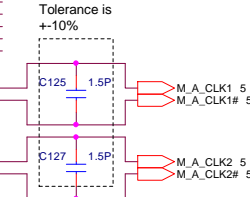
EXAMPLE

10R=10ohm(0402)
10A=10ohm(0603)
10B=10ohm(0805)
10C=10ohm(1206)
10/F=10ohm(0402 and 1%)





M.A.DQ63	AA12	MA_DATA[63]
M.A.DQ62	AB12	MA_DATA[62]
M.A.DQ61	AB14	MA_DATA[61]
M.A.DQ60	AB14	MA_DATA[60]
M.A.DQ59	W11	MA_DATA[59]
M.A.DQ58	Y12	MA_DATA[58]
M.A.DQ57	AD13	MA_DATA[57]
M.A.DQ56	AB13	MA_DATA[56]
M.A.DQ55	AD15	MA_DATA[55]
M.A.DQ54	AB15	MA_DATA[54]
M.A.DQ53	AB17	MA_DATA[53]
M.A.DQ52	Y17	MA_DATA[52]
M.A.DQ51	Y14	MA_DATA[51]
M.A.DQ50	W14	MA_DATA[50]
M.A.DQ49	W16	MA_DATA[49]
M.A.DQ48	AD17	MA_DATA[48]
M.A.DQ47	Y18	MA_DATA[47]
M.A.DQ46	AD19	MA_DATA[46]
M.A.DQ45	AD21	MA_DATA[45]
M.A.DQ44	AB21	MA_DATA[44]
M.A.DQ43	AB18	MA_DATA[43]
M.A.DQ42	AA18	MA_DATA[42]
M.A.DQ41	AA20	MA_DATA[41]
M.A.DQ40	Y20	MA_DATA[40]
M.A.DQ39	AA22	MA_DATA[39]
M.A.DQ38	Y22	MA_DATA[38]
M.A.DQ37	W21	MA_DATA[37]
M.A.DQ36	Y22	MA_DATA[36]
M.A.DQ35	AA21	MA_DATA[35]
M.A.DQ34	AB22	MA_DATA[34]
M.A.DQ33	AB24	MA_DATA[33]
M.A.DQ32	Y24	MA_DATA[32]
M.A.DQ31	H22	MA_DATA[31]
M.A.DQ30	H20	MA_DATA[30]
M.A.DQ29	E22	MA_DATA[29]
M.A.DQ28	E21	MA_DATA[28]
M.A.DQ27	J15	MA_DATA[27]
M.A.DQ26	H24	MA_DATA[26]
M.A.DQ25	F22	MA_DATA[25]
M.A.DQ24	F20	MA_DATA[24]
M.A.DQ23	C22	MA_DATA[23]
M.A.DQ22	B22	MA_DATA[22]
M.A.DQ21	F18	MA_DATA[21]
M.A.DQ20	E18	MA_DATA[20]
M.A.DQ19	D20	MA_DATA[19]
M.A.DQ18	D22	MA_DATA[18]
M.A.DQ17	C19	MA_DATA[17]
M.A.DQ16	G18	MA_DATA[16]
M.A.DQ15	G17	MA_DATA[15]
M.A.DQ14	E14	MA_DATA[14]
M.A.DQ13	E14	MA_DATA[13]
M.A.DQ12	E14	MA_DATA[12]
M.A.DQ11	H17	MA_DATA[11]
M.A.DQ10	E17	MA_DATA[10]
M.A.DQ9	E15	MA_DATA[9]
M.A.DQ8	H15	MA_DATA[8]
M.A.DQ7	E13	MA_DATA[7]
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M.A.DQ5	C13	MA_DATA[5]
M.A.DQ4	H11	MA_DATA[4]
M.A.DQ3	G14	MA_DATA[3]
M.A.DQ2	H14	MA_DATA[2]
M.A.DQ1	F12	MA_DATA[1]
M.A.DQ0	G12	MA_DATA[0]



TRACE FROM CAP TO CPU MUST BE LESS THAN 1200MILS MAX NECKDOWN TO & FROM CAPS IS 500MILS

MA_BANK[2]	K22	M.A.BA2
MA_BANK[1]	R20	M.A.BA1
MA_BANK[0]	T22	M.A.BA0

MAO_CS#3	V19	M.A.CS#3
MAO_CS#2	C22	M.A.CS#2
MAO_CS#1	V22	M.A.CS#1
MAO_CS#0	T19	M.A.CS#0

MAO_CKE[1]	J20	M.A.CKE1
MAO_CKE[0]	J21	M.A.CKE0

MAO_ODT[1]	V20	M.A.ODT1
MAO_ODT[0]	U19	M.A.ODT0

AMD S1 SOCKET

M.B.DQ63	AD11	MB_DATA[63]
M.B.DQ62	AC16	MB_DATA[62]
M.B.DQ61	AE14	MB_DATA[61]
M.B.DQ60	AE14	MB_DATA[60]
M.B.DQ59	Y11	MB_DATA[59]
M.B.DQ58	Y11	MB_DATA[58]
M.B.DQ57	AD12	MB_DATA[57]
M.B.DQ56	AF13	MB_DATA[56]
M.B.DQ55	AF15	MB_DATA[55]
M.B.DQ54	AF16	MB_DATA[54]
M.B.DQ53	AC18	MB_DATA[53]
M.B.DQ52	AF19	MB_DATA[52]
M.B.DQ51	AD14	MB_DATA[51]
M.B.DQ50	AC14	MB_DATA[50]
M.B.DQ49	AF18	MB_DATA[49]
M.B.DQ48	AD18	MB_DATA[48]
M.B.DQ47	AD20	MB_DATA[47]
M.B.DQ46	AC20	MB_DATA[46]
M.B.DQ45	AF23	MB_DATA[45]
M.B.DQ44	AF24	MB_DATA[44]
M.B.DQ43	AF20	MB_DATA[43]
M.B.DQ42	AE20	MB_DATA[42]
M.B.DQ41	AD22	MB_DATA[41]
M.B.DQ40	AC22	MB_DATA[40]
M.B.DQ39	AE25	MB_DATA[39]
M.B.DQ38	AD26	MB_DATA[38]
M.B.DQ37	AD25	MB_DATA[37]
M.B.DQ36	AE26	MB_DATA[36]
M.B.DQ35	AE24	MB_DATA[35]
M.B.DQ34	AD24	MB_DATA[34]
M.B.DQ33	AA23	MB_DATA[33]
M.B.DQ32	AA24	MB_DATA[32]
M.B.DQ31	G24	MB_DATA[31]
M.B.DQ30	G23	MB_DATA[30]
M.B.DQ29	D26	MB_DATA[29]
M.B.DQ28	C26	MB_DATA[28]
M.B.DQ27	G26	MB_DATA[27]
M.B.DQ26	G25	MB_DATA[26]
M.B.DQ25	E24	MB_DATA[25]
M.B.DQ24	E23	MB_DATA[24]
M.B.DQ23	C24	MB_DATA[23]
M.B.DQ22	B24	MB_DATA[22]
M.B.DQ21	C20	MB_DATA[21]
M.B.DQ20	B20	MB_DATA[20]
M.B.DQ19	C25	MB_DATA[19]
M.B.DQ18	D24	MB_DATA[18]
M.B.DQ17	A21	MB_DATA[17]
M.B.DQ16	D20	MB_DATA[16]
M.B.DQ15	D18	MB_DATA[15]
M.B.DQ14	C18	MB_DATA[14]
M.B.DQ13	D14	MB_DATA[13]
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M.B.DQ10	A19	MB_DATA[10]
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M.B.DQ4	G11	MB_DATA[4]
M.B.DQ3	B14	MB_DATA[3]
M.B.DQ2	A14	MB_DATA[2]
M.B.DQ1	A11	MB_DATA[1]
M.B.DQ0	C11	MB_DATA[0]

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M.B.A12	L23	MB_ADD[12]
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M.B.A3	N26	MB_ADD[3]
M.B.A2	P24	MB_ADD[2]
M.B.A1	P26	MB_ADD[1]
M.B.A0	T24	MB_ADD[0]

MB_DM[7]	AD12	M.B.DQM7
MB_DM[6]	AC16	M.B.DQM6
MB_DM[5]	AE22	M.B.DQM5
MB_DM[4]	AB26	M.B.DQM4
MB_DM[3]	E25	M.B.DQM3
MB_DM[2]	A22	M.B.DQM2
MB_DM[1]	B16	M.B.DQM1
MB_DM[0]	A12	M.B.DQM0

MB_DQS[7]	AF12	M.B.DQS7
MB_DQS[6]	AE16	M.B.DQS6
MB_DQS[5]	AF21	M.B.DQS5
MB_DQS[4]	AC25	M.B.DQS4
MB_DQS[3]	F26	M.B.DQS3
MB_DQS[2]	A24	M.B.DQS2
MB_DQS[1]	D16	M.B.DQS1
MB_DQS[0]	C12	M.B.DQS0

MB_DQS[7]	AE12	M.B.DQS7
MB_DQS[6]	AE16	M.B.DQS6
MB_DQS[5]	AF21	M.B.DQS5
MB_DQS[4]	AC26	M.B.DQS4
MB_DQS[3]	E26	M.B.DQS3
MB_DQS[2]	A23	M.B.DQS2
MB_DQS[1]	C16	M.B.DQS1
MB_DQS[0]	B12	M.B.DQS0

MB_BANK[2]	K26	M.B.BA2
MB_BANK[1]	T26	M.B.BA1
MB_BANK[0]	U26	M.B.BA0

MB_RAS#	U24	M.B.RAS#
MB_CAS#	V26	M.B.CAS#
MB_WE#	U22	M.B.WE#

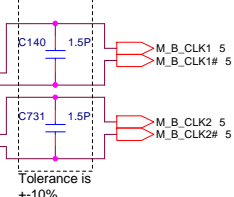
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MBO_CS#2	J24	M.B.CS#2
MBO_CS#1	W24	M.B.CS#1
MBO_CS#0	U23	M.B.CS#0

MBO_CKE[1]	H26	M.B.CKE1
MBO_CKE[0]	J23	M.B.CKE0

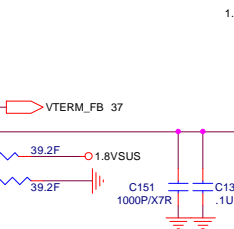
MBO_ODT[1]	W23	M.B.ODT1
MBO_ODT[0]	W26	M.B.ODT0

M.B.DQM7	0	M.B.DQM7
M.B.DQM6	0	M.B.DQM6
M.B.DQM5	0	M.B.DQM5
M.B.DQM4	0	M.B.DQM4
M.B.DQM3	0	M.B.DQM3
M.B.DQM2	0	M.B.DQM2
M.B.DQM1	0	M.B.DQM1
M.B.DQM0	0	M.B.DQM0

TRACE FROM CAP TO CPU MUST BE LESS THAN 1200MILS MAX NECKDOWN TO & FROM CAPS IS 500MILS



Tolerance is +/-10%



C51MVREF : W = 20MIL AND SPACE = 20MIL

5 M.A.DQ[63..0]	M.A.DQ[63..0]
4.5 M.A.A[15..0]	M.A.A[15..0]

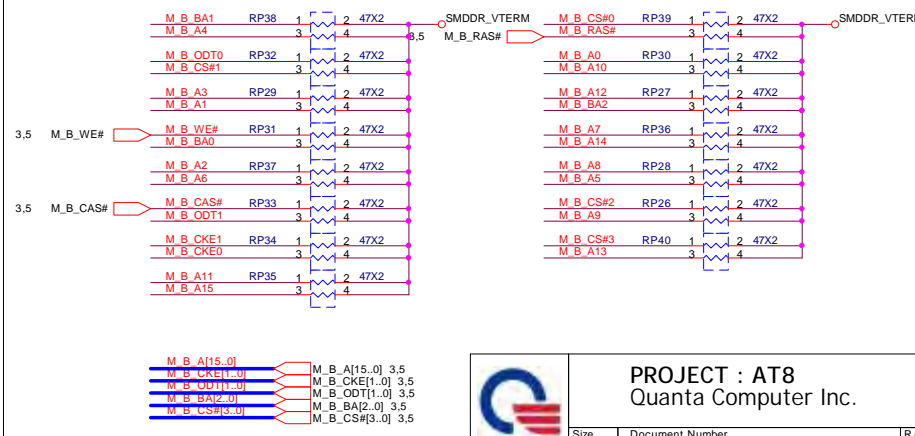
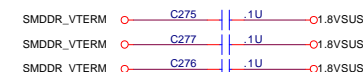
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M.A.DQM6	0	M.A.DQM6
M.A.DQM5	0	M.A.DQM5
M.A.DQM4	0	M.A.DQM4
M.A.DQM3	0	M.A.DQM3
M.A.DQM2	0	M.A.DQM2
M.A.DQM1	0	M.A.DQM1
M.A.DQM0	0	M.A.DQM0

5 M.B.DQ[63..0]	M.B.DQ[63..0]
4.5 M.B.A[15..0]	M.B.A[15..0]

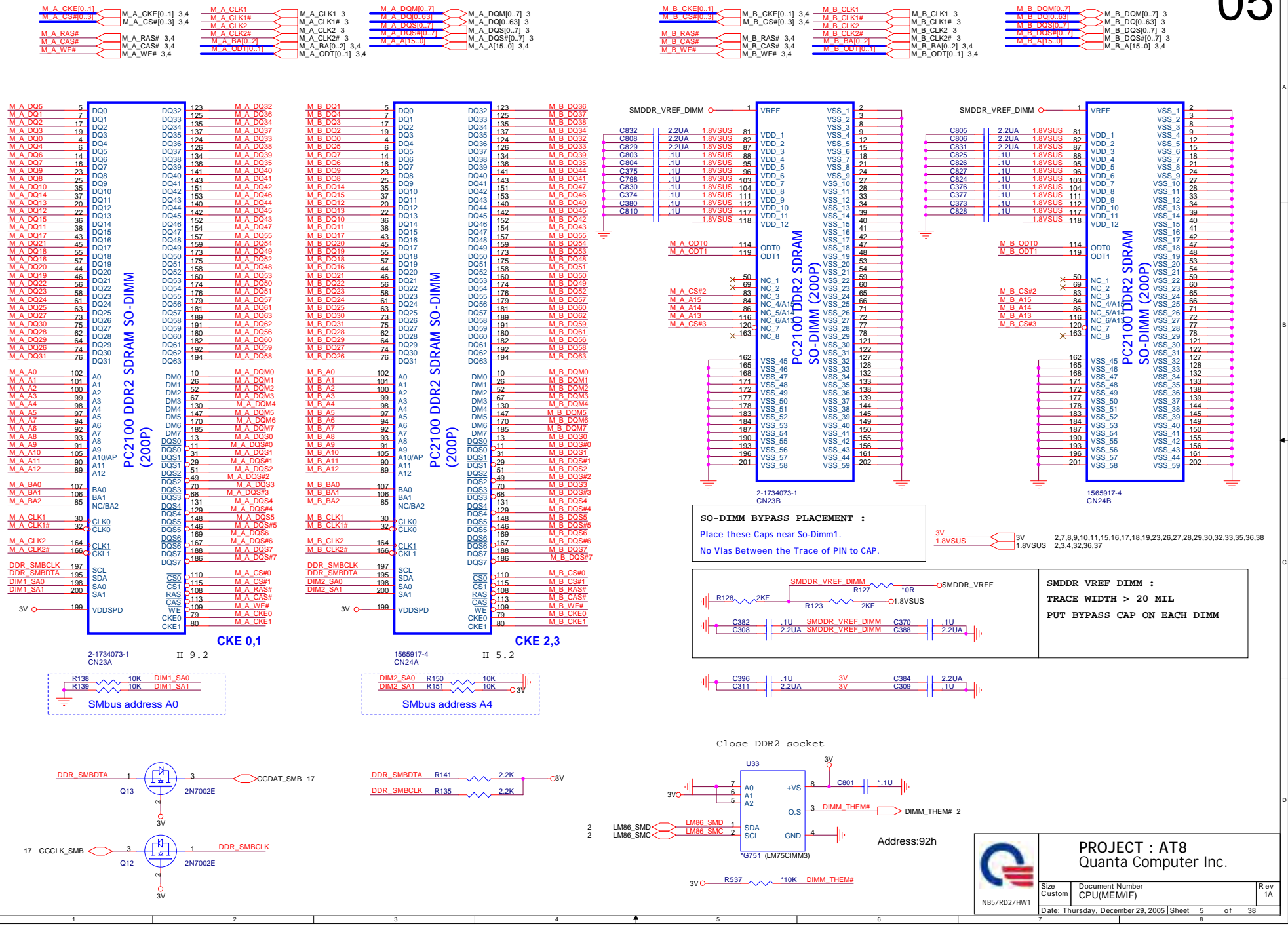
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M.B.DQM3	0	M.B.DQM3
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M.B.DQM0	0	M.B.DQM0

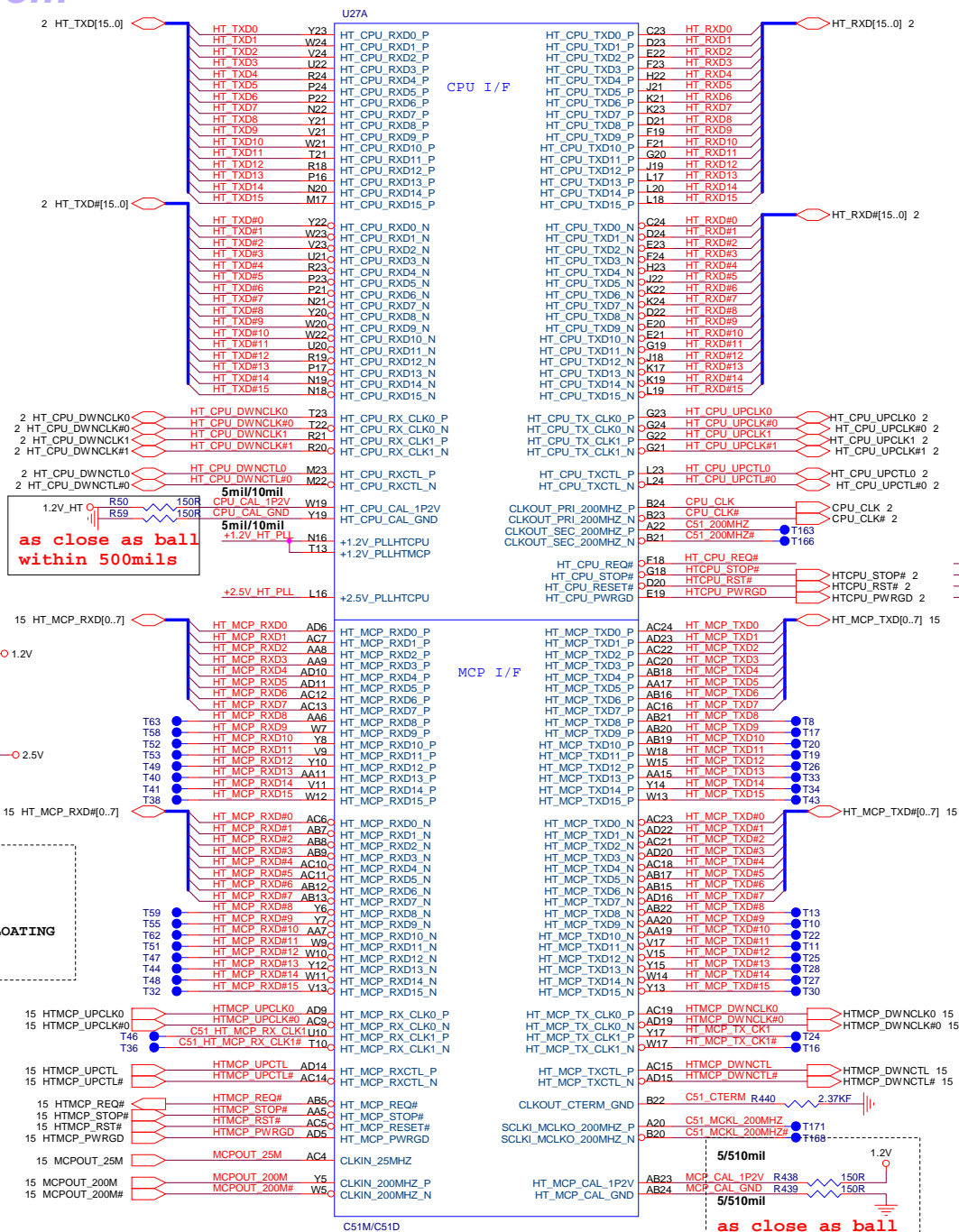
PROJECT : AT8
Quanta Computer Inc.

Size Custom	Document Number CPU(MEM)/F	Rev 1A
Date: Thursday, December 29, 2005 Sheet 3 of 38		



WWW.AliSaler.Com





```
#note from nv design
guide
```

Pull-Hi for rise time

Time

STUFF : 4X4 HT LINK/UNUSED PIN FLOATING
NON-STUFF : 8X8 HT LINK(DEFAULT)

as close as ball
within 500mils

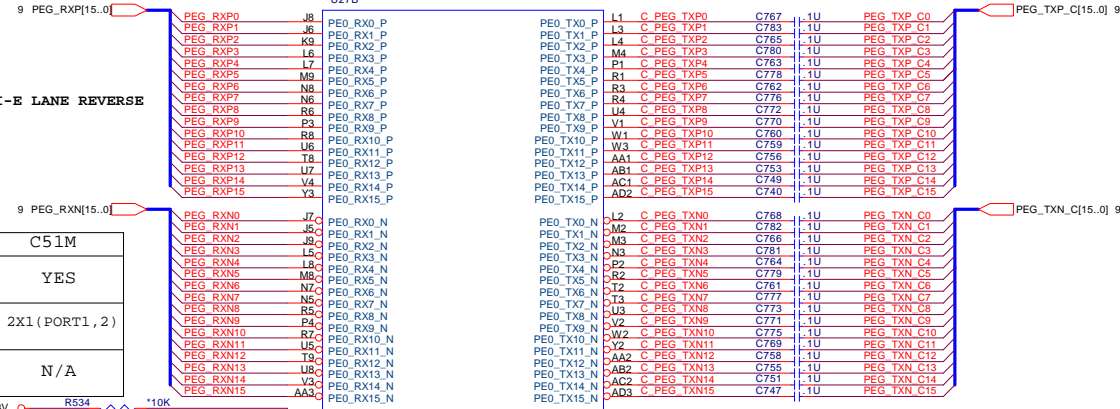


PROJECT : AT8
Quanta Computer Inc.

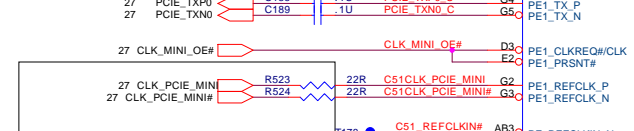
Size Custom	Document Number C51MV (HT LINK)	Re 1
Date: Thursday, December 29, 2005		Sheet 6 of 38

C51D SUPPORT PCI-E LANE REVERSE

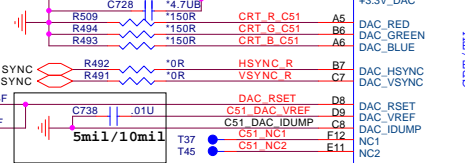
	C51D	C51M
GPU...	N/A	YES
PCI-E	1X16(PORT0) 2X1(PORT1,2)	2X1(PORT1,2)
PE0_PRST#	LOW	N/A



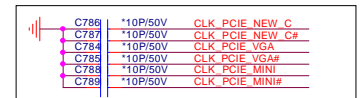
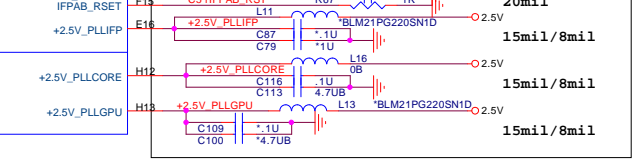
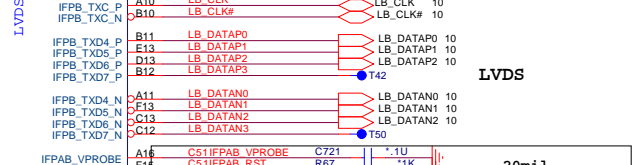
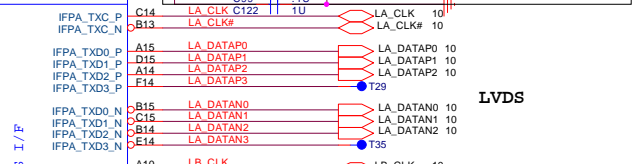
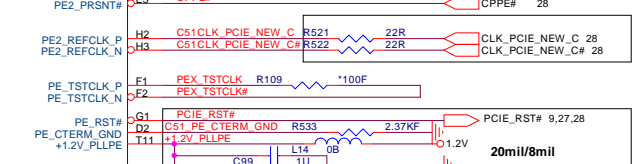
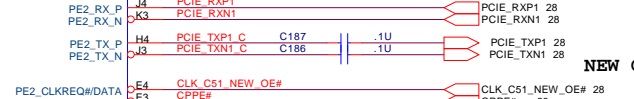
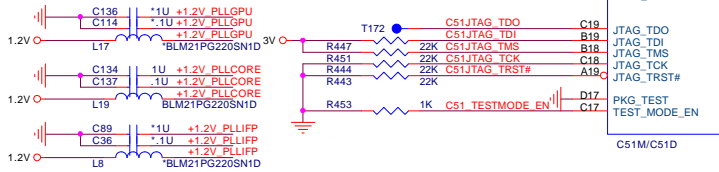
MINI CARD



15mil/8mil



only required for C51M TV-OUT



C51D Unused Power Ball Terminations	
C51D Signal Name	Termination
+1.2V_PLLGPU	Leave NC
+1.2V_PLLIFP	Leave NC
+2.5V_IFPA	Leave NC
+2.5V_IFPB	Leave NC
+2.5V_PLLIFP	Leave NC
+2.5V_PLLGPU	Leave NC
+3.3V_DAC	to GND directly
DAC_RST	1240hm to GND
DAC_VREF	0.01u to GND

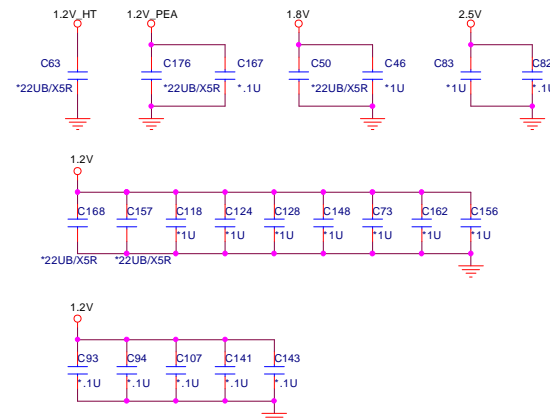
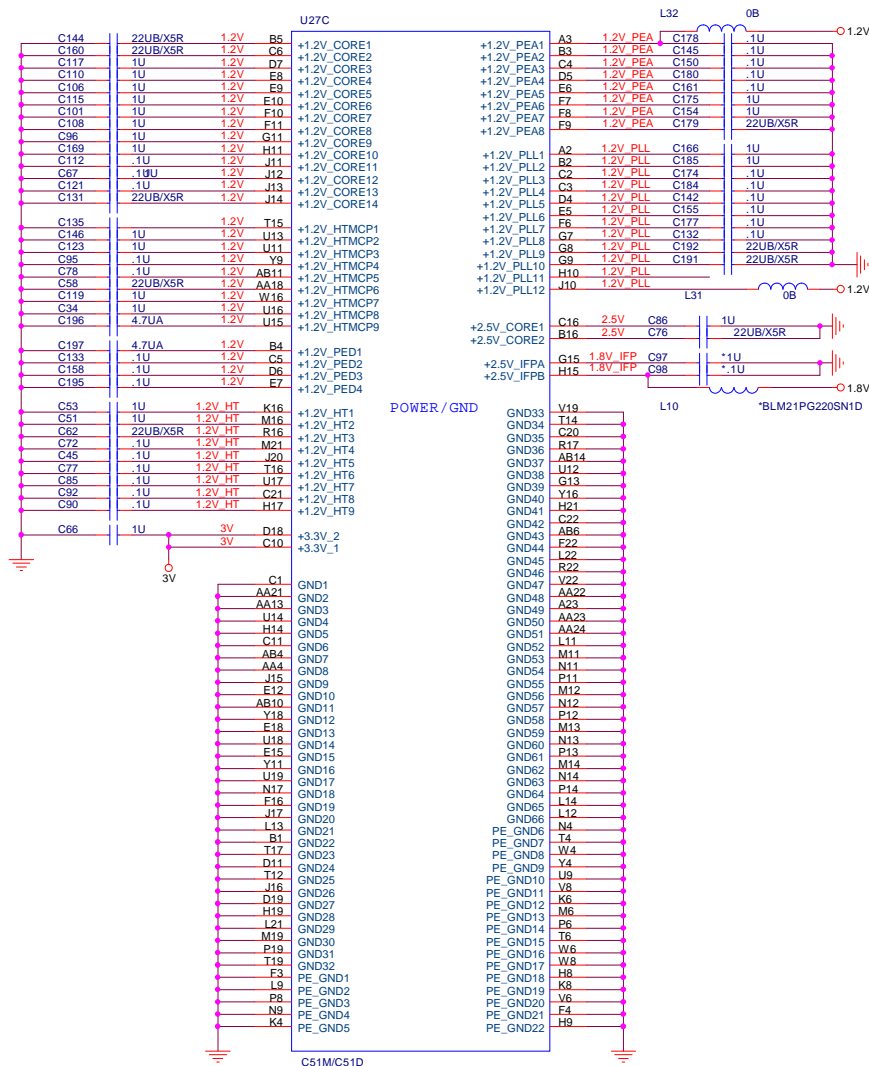
TRACE WIDTH :	
C51_PE_CTERM_GND	5mil/5mil
C51IFPAB_VPROBE	
C51IFPAB_RST	
C51_DAC_VREF	5mil/10mil
+1.2V_PLLPE	+1.2V_PLLIFP
+2.5V_PLLIFP	+1.2V_PLLCORE
+2.5V_PLLCORE	+1.2V_PLLGPU
+2.5V_PLLGPU	



PROJECT : AT8
Quanta Computer Inc.

Size Custom	Document Number C51M (PCI-E/LCD/RGB)	Rev 1A
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C51M/C51D POWER PLANE/GND & BYPASS

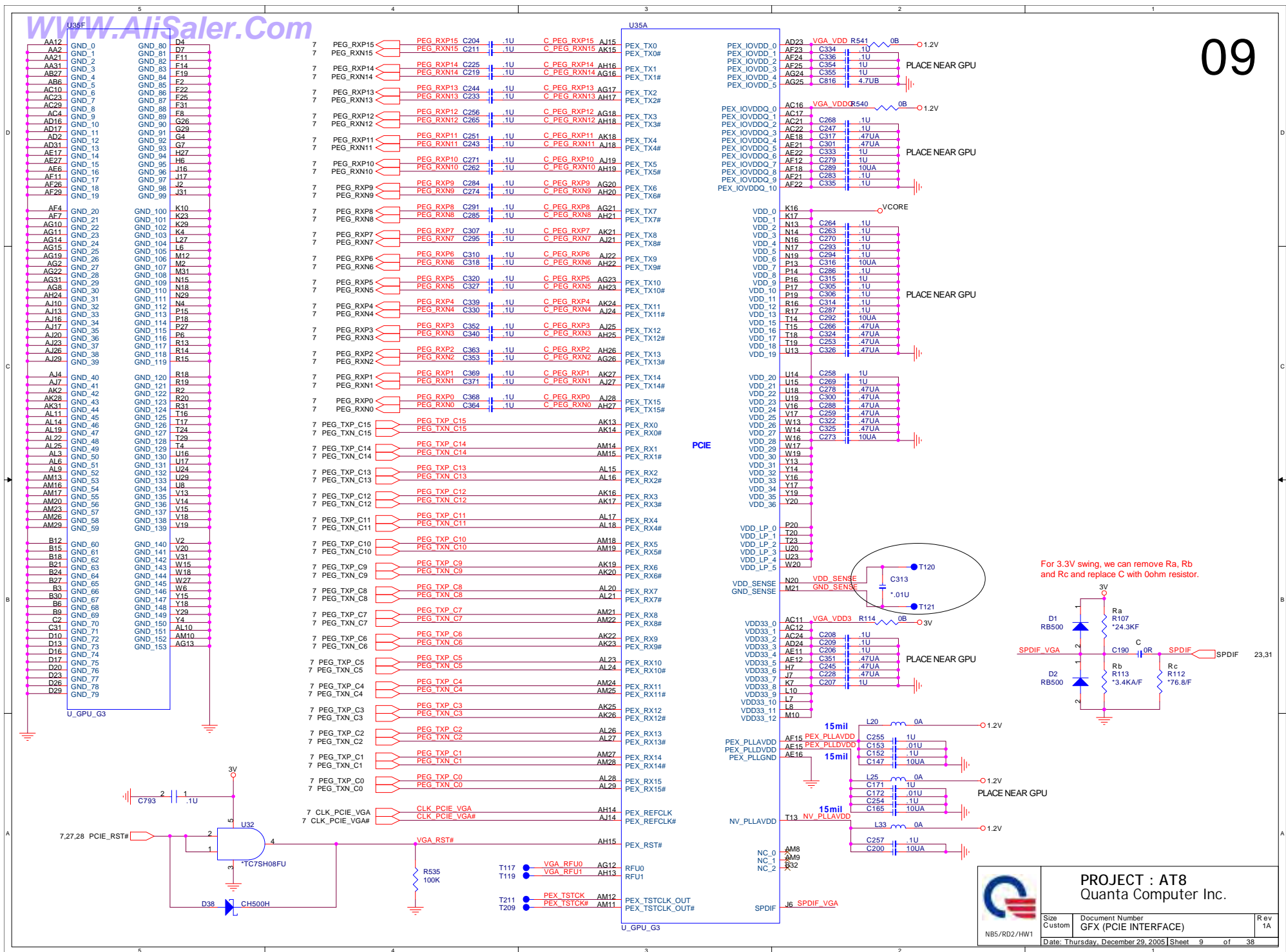


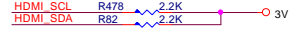
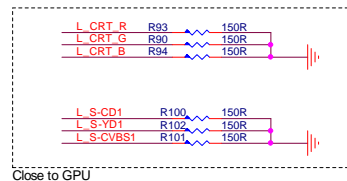
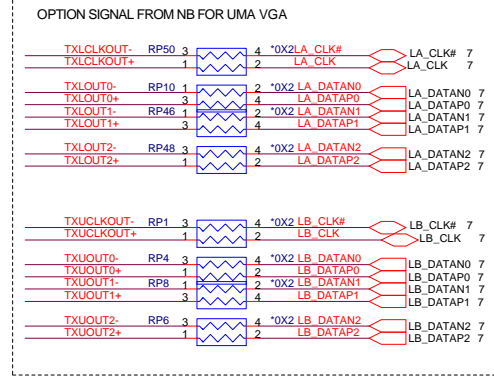
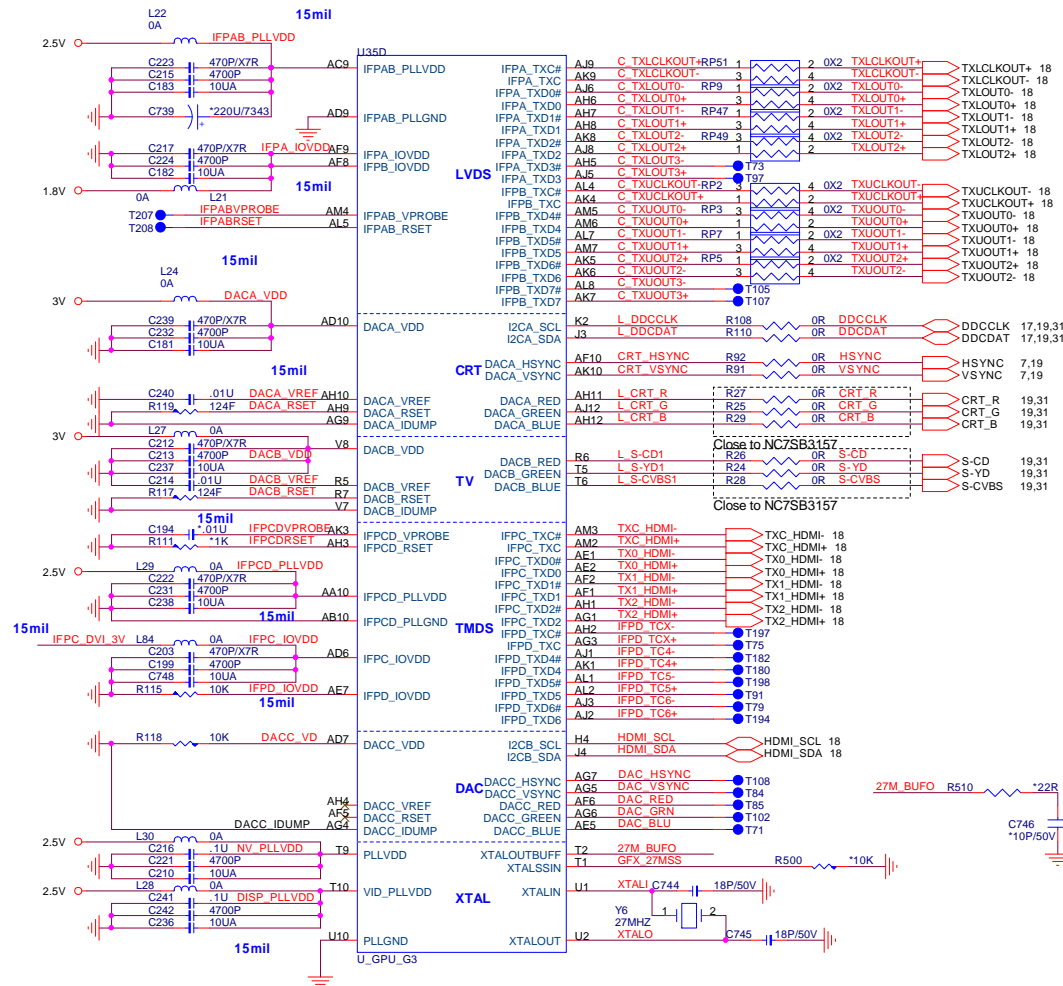
NB5/RD2/HW1

PROJECT : AT8
Quanta Computer Inc.

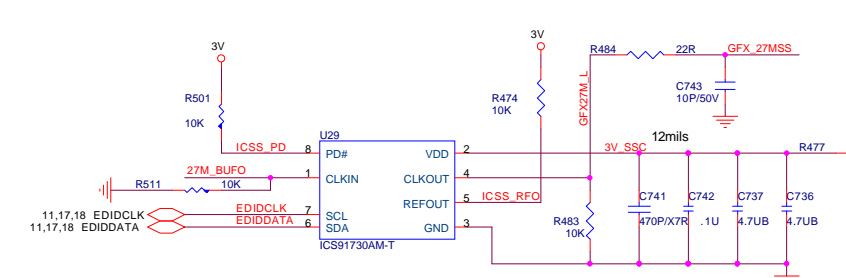
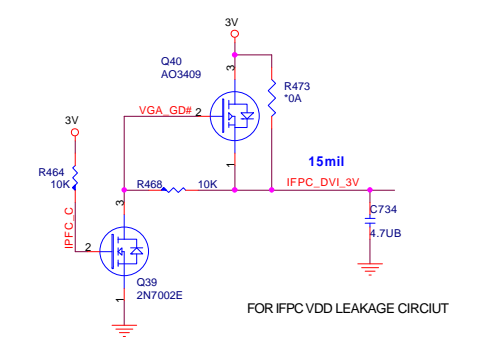
Size
CustomDocument Number
C51MV (POWER/GND)Rev
1A

Date: Wednesday, January 11, 2006 Sheet 8 of 38





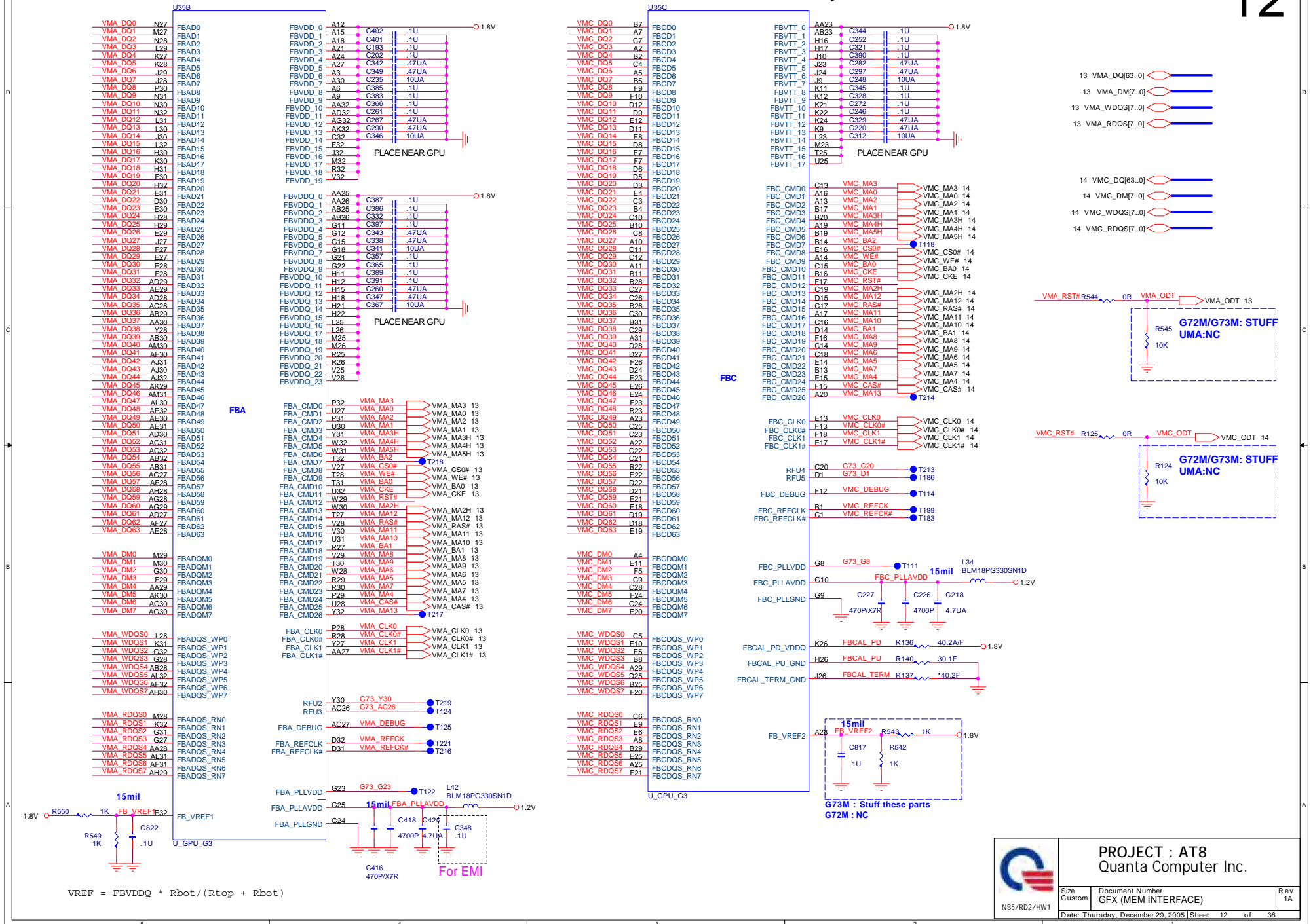
SPREAD SPECTRUM



PROJECT : AT8
Quantas Computer Inc.

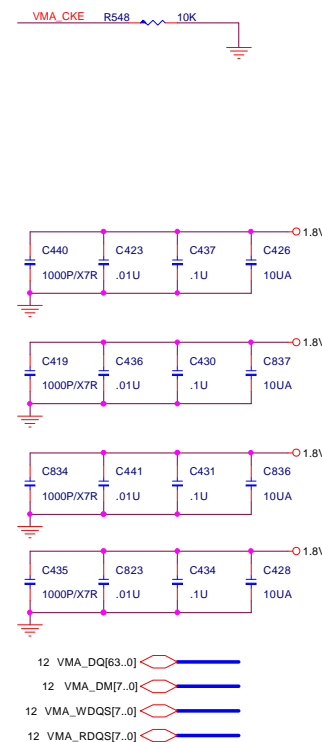
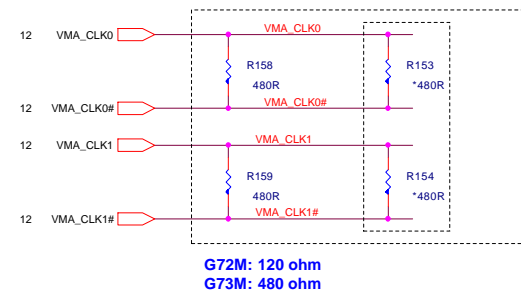
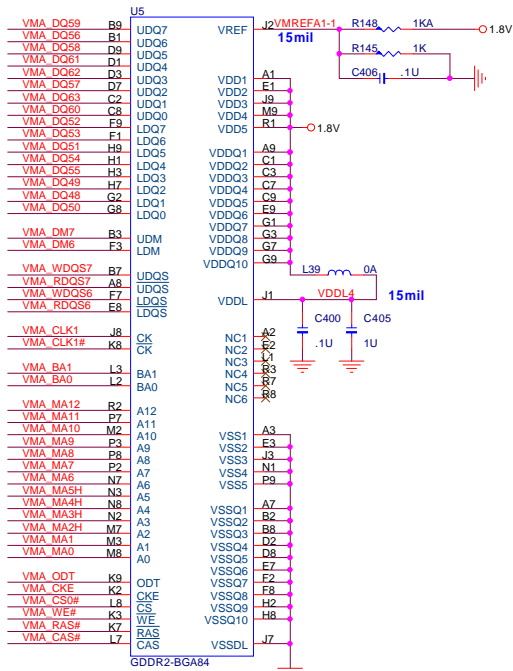
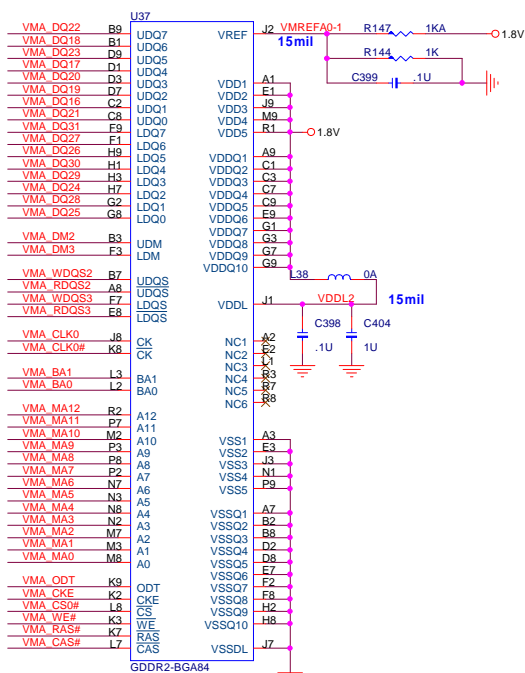
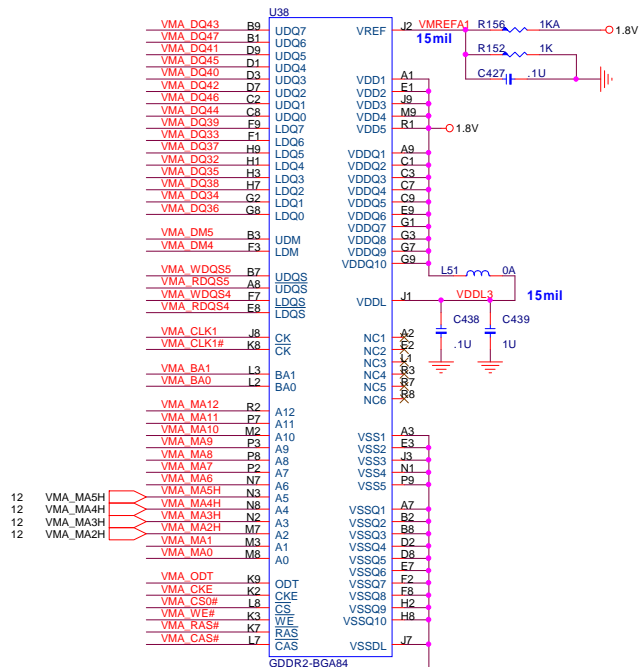
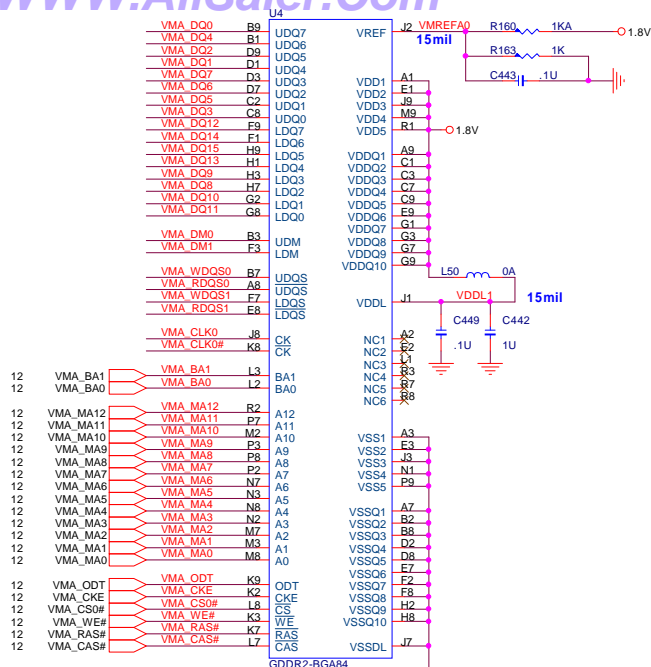
Size Custom	Document Number GFX(LVDS, CRT, TV)	Rev 1A
Date: Thursday, December 29, 2005	Sheet 10 of 38	

Channel C is available on G73M only



PROJECT : AT8
Quanta Computer Inc.

Size	Document Number	Rev
Custom	GFX (MEM INTERFACE)	1A
Date: Thursday, December 29, 2005	Sheet 12 of 38	

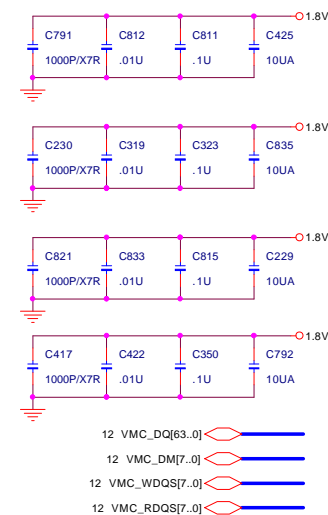
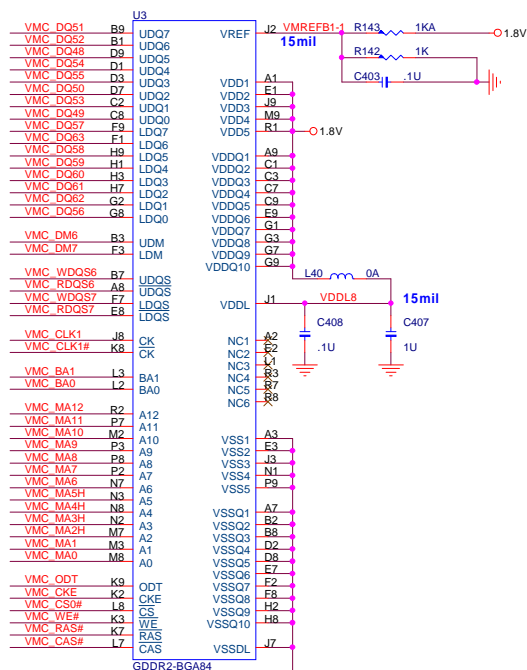
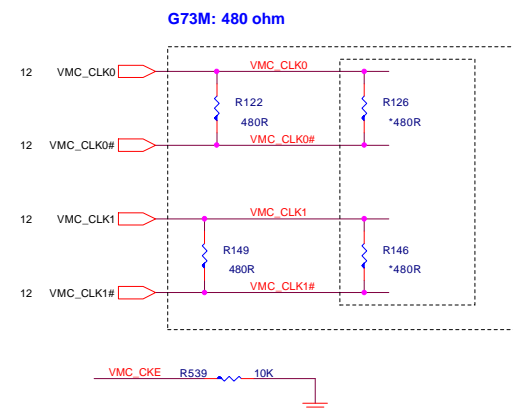


256Mb : AKD5JGAT^05
512Mb : AKD59G-T^01



PROJECT : AT8
Quanta Computer Inc.

Size Custom	Document Number VRAM-1 (GDDR2)	Rev 1A
Date: Thursday, December 29, 2005 Sheet 13 of 38		

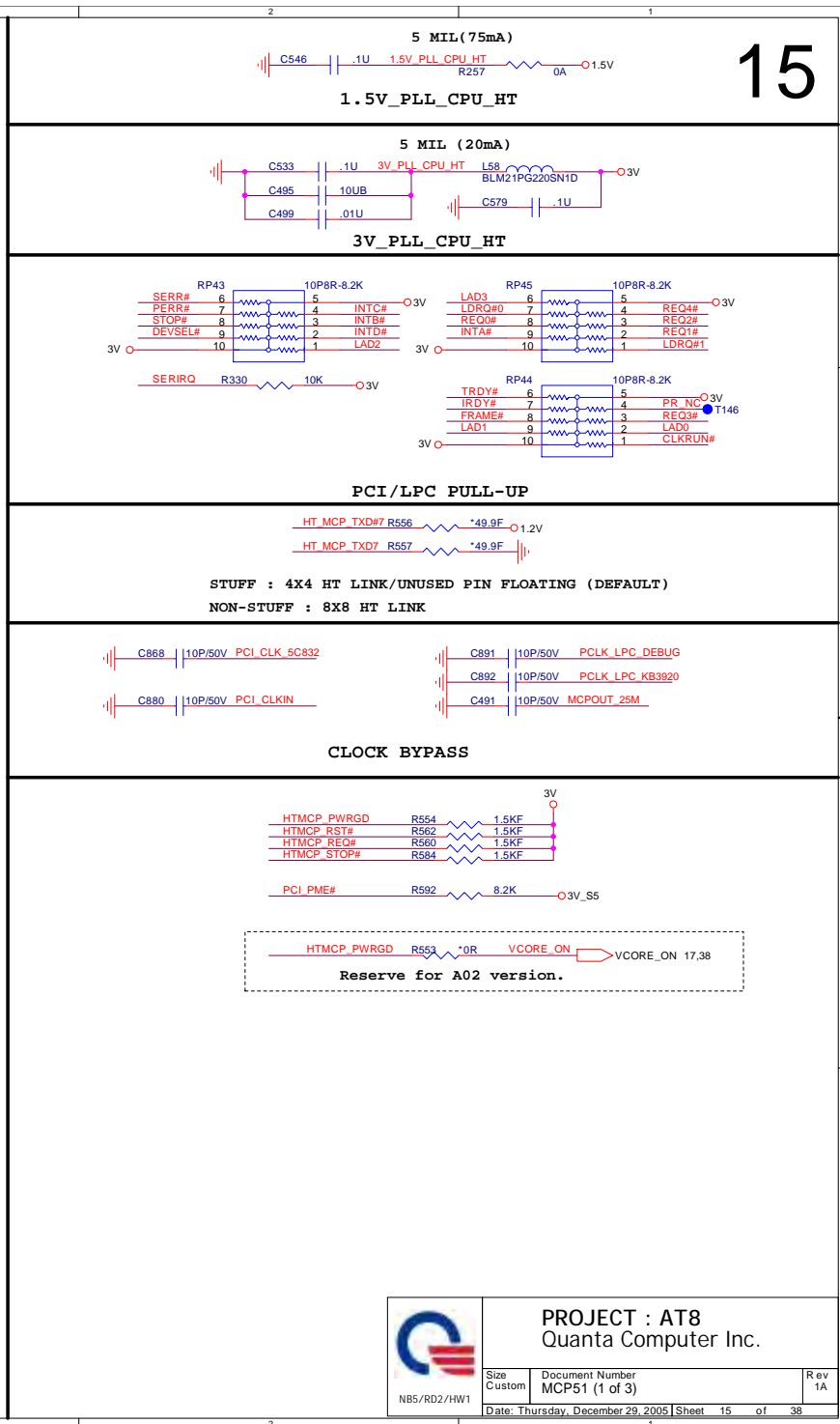
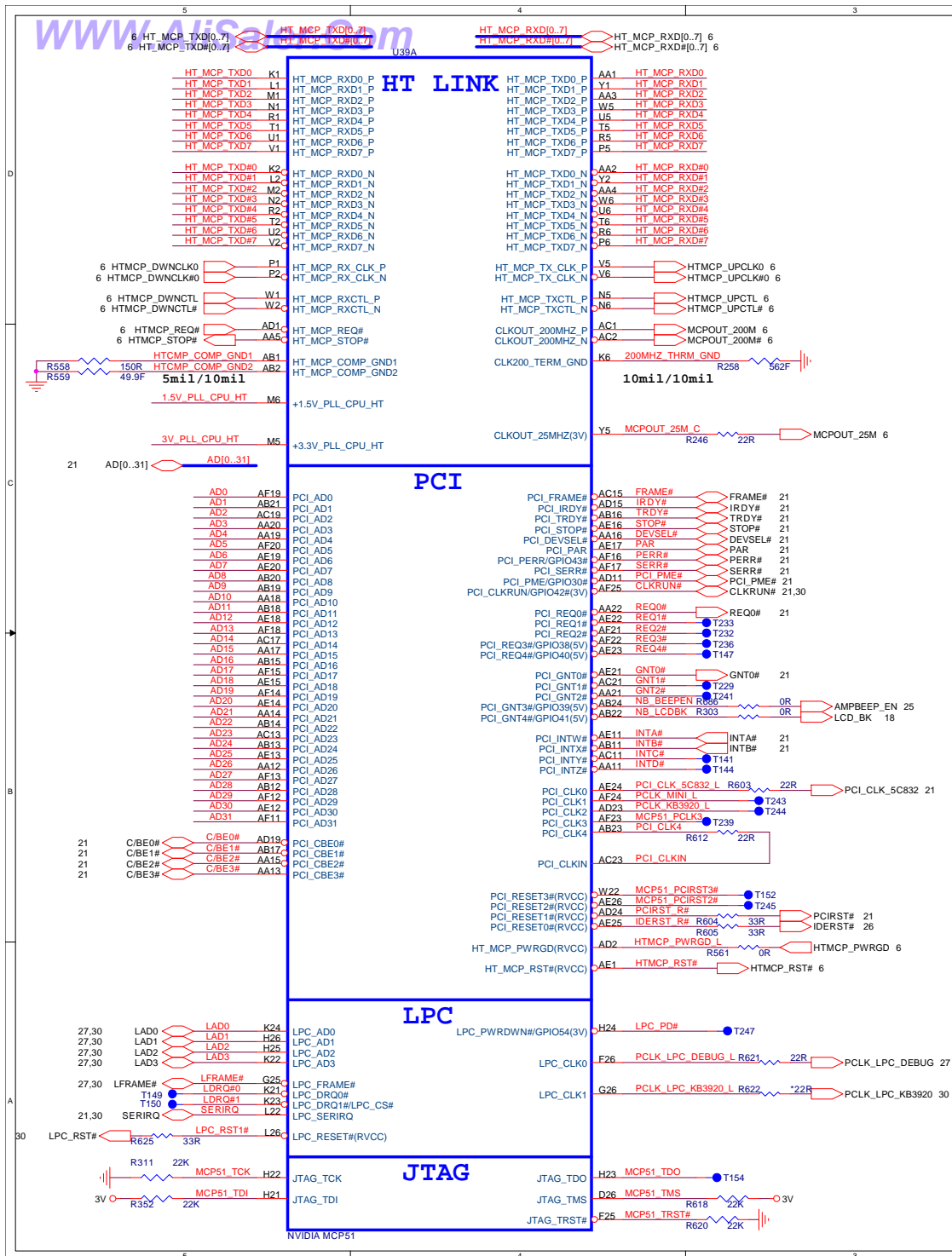


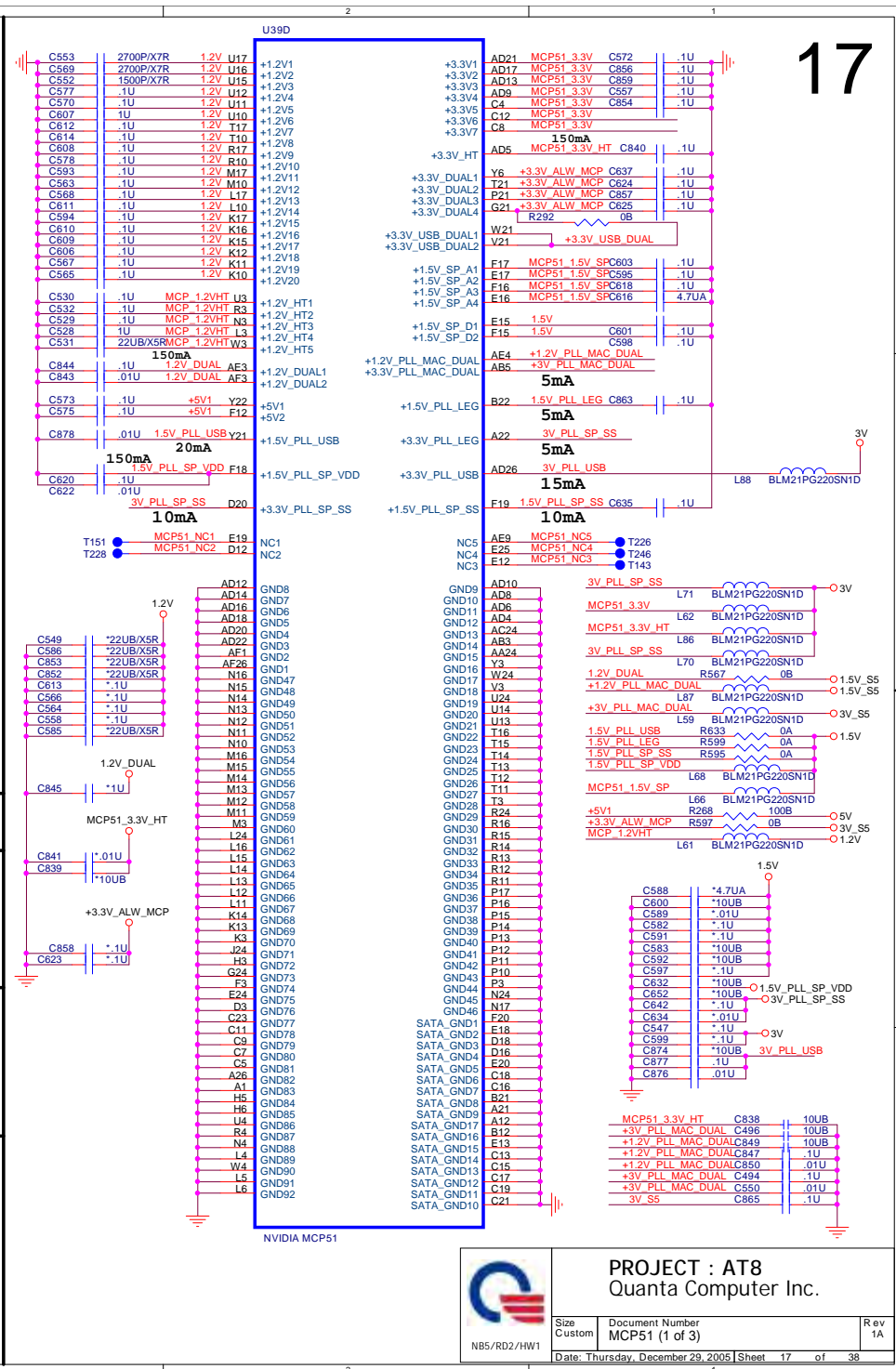
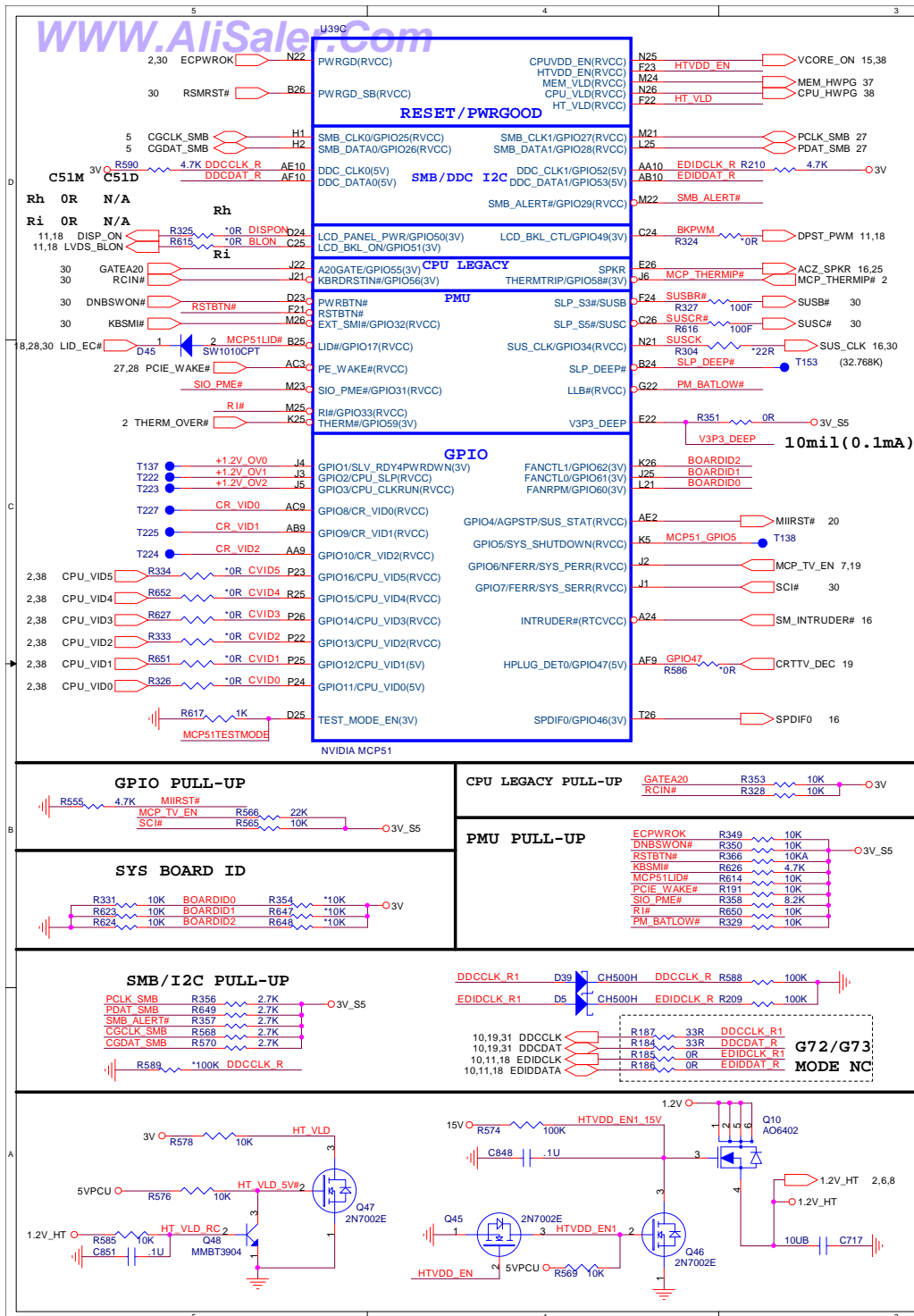
256Mb : AKD5JGAT^05
512Mb : AKD59G-TA01

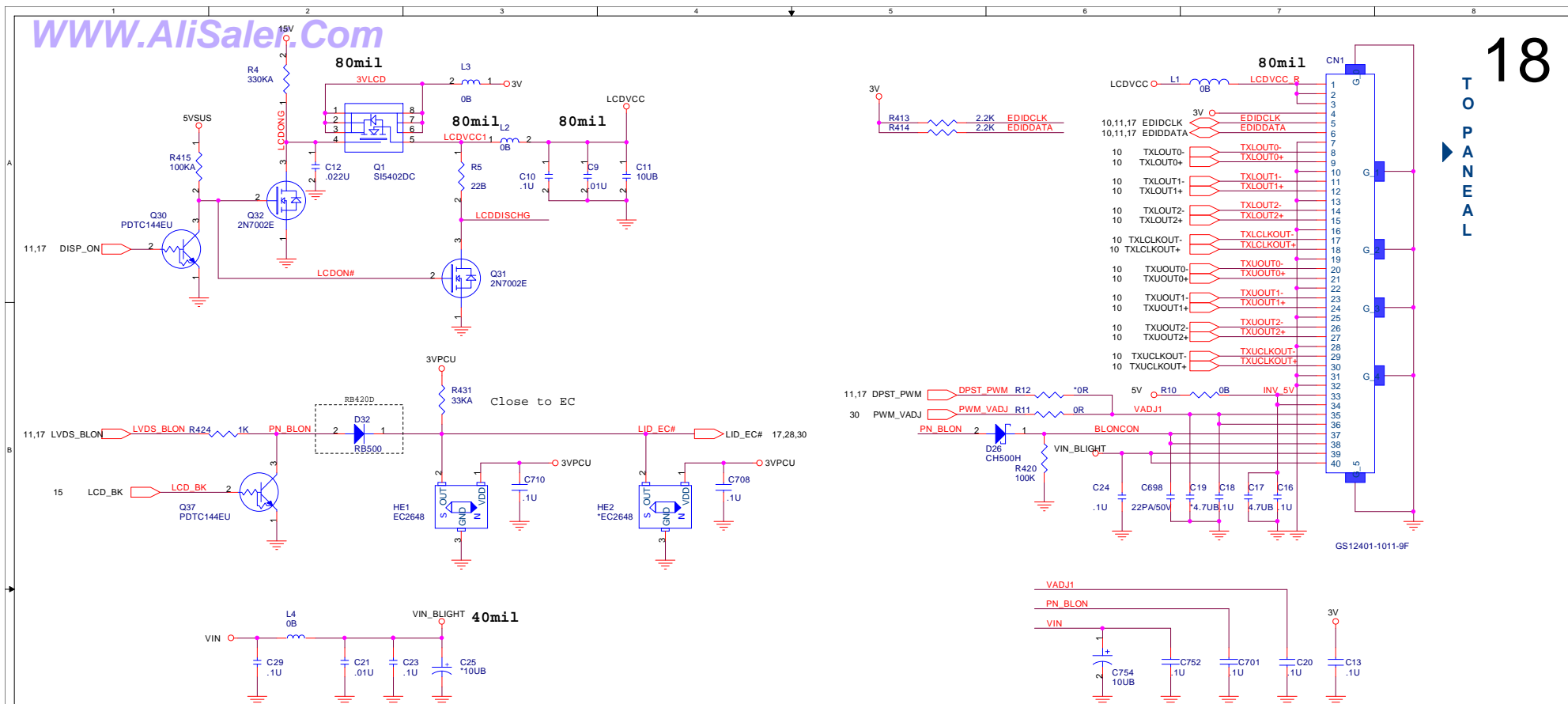


PROJECT : AT8
Quanta Computer Inc.

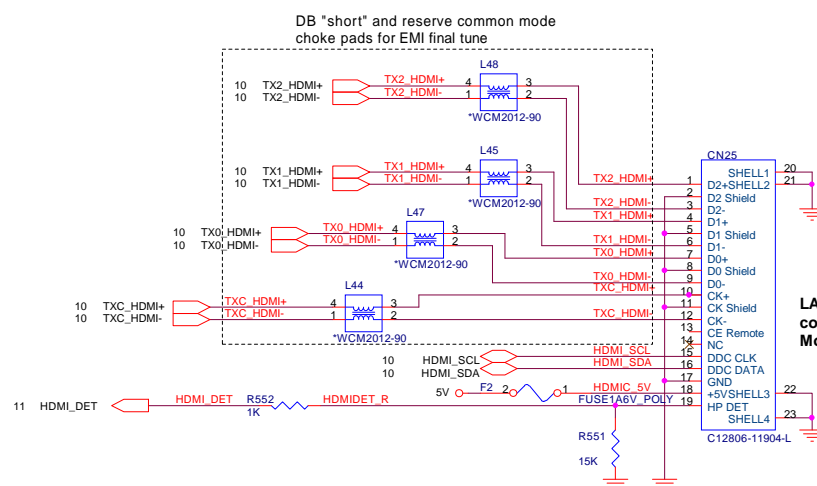
Size Custom	Document Number VRAM-2 (GDDR2)
Date: Thursday, December 29, 2005	Sheet 14 of 38







HDMI PORT



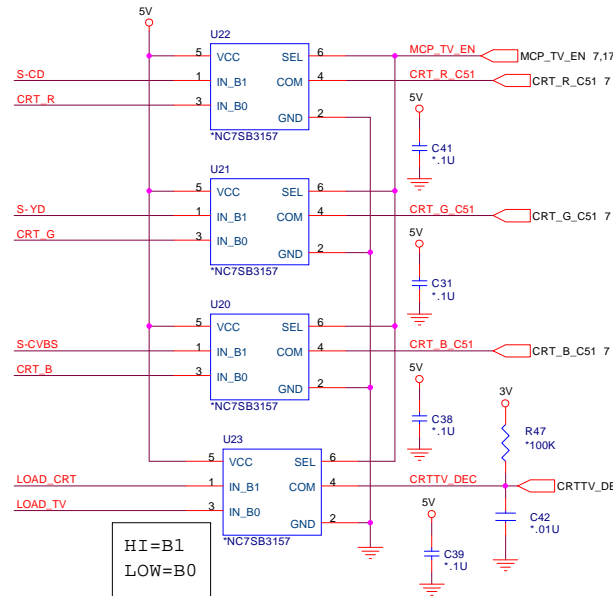
LAYOUT must support connectors from JAE, Molex and Acon.



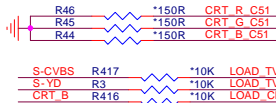
PROJECT : AT8
Quanta Computer Inc.

Size Custom	Document Number LCD CONN,HDMI CONN	Re 1
Date: Thursday, December 29, 2005		Sheet 18 of 38

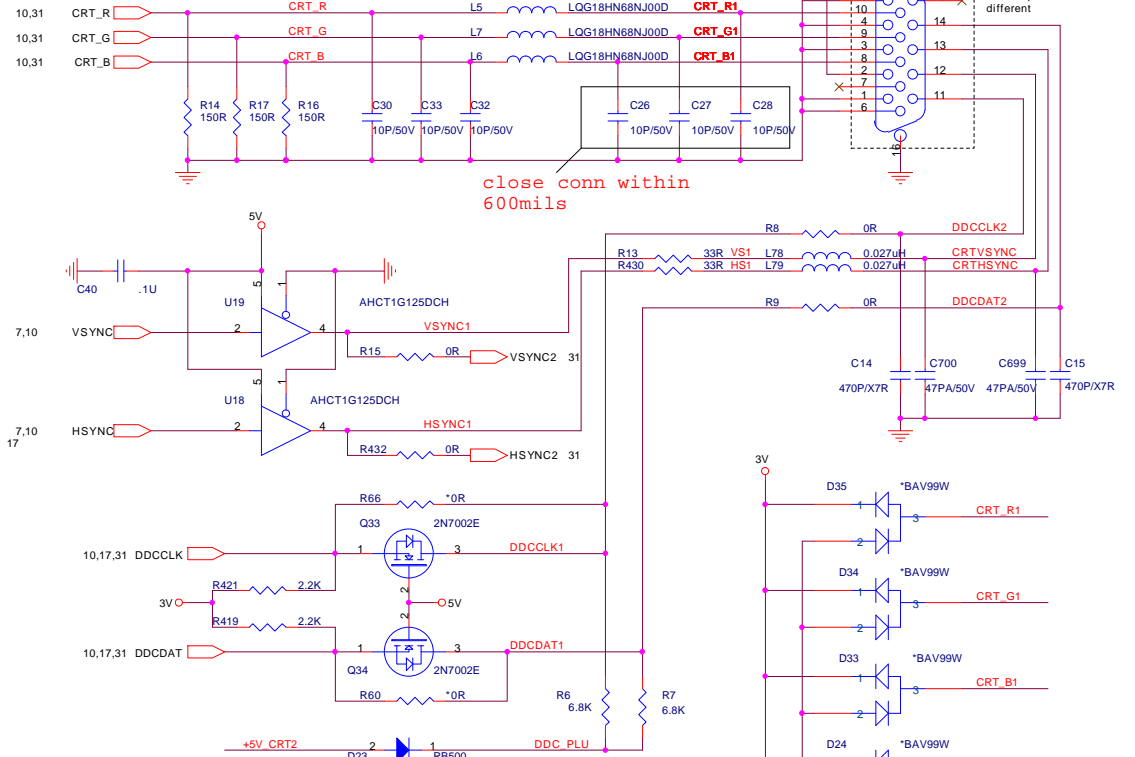
CRT PORT



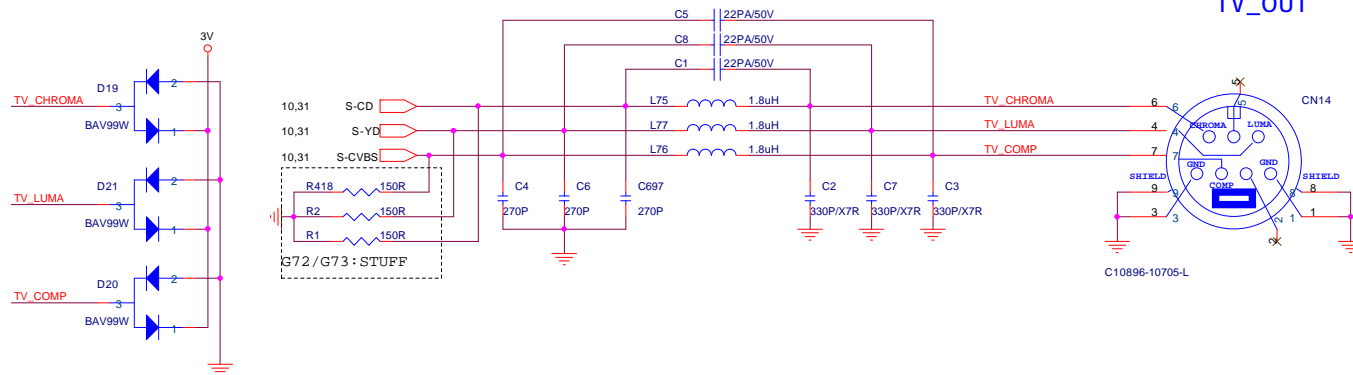
close within 600mils




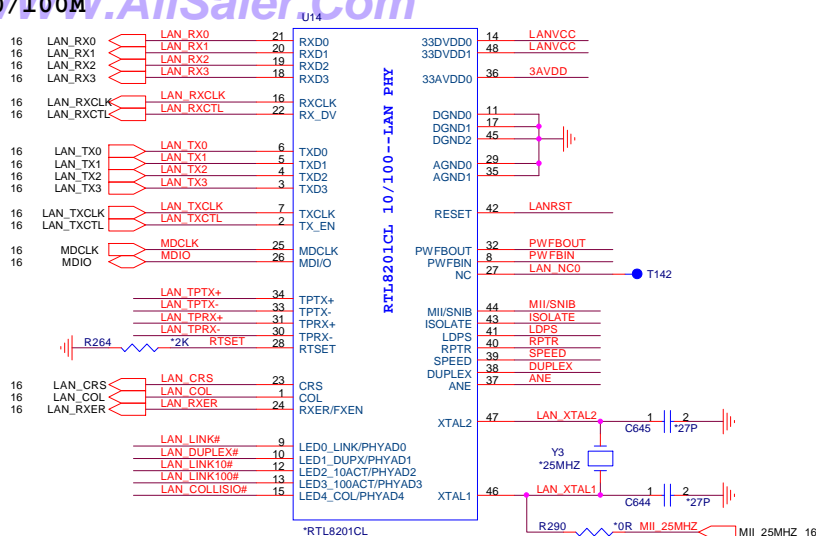
75-R as possible as closed to SB



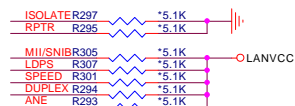
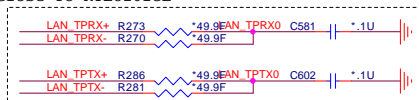
TV_OUT



	PROJECT : AT8		
	Quanta Computer Inc.		
Size Custom	Document Number	Rev 1A	
NB5/RD2/HW1	CRT_TV_OUT		
Date: Thursday, December 29, 2005	Sheet 19 of 38		



Close To RTL8201CL



PHY Address:11111

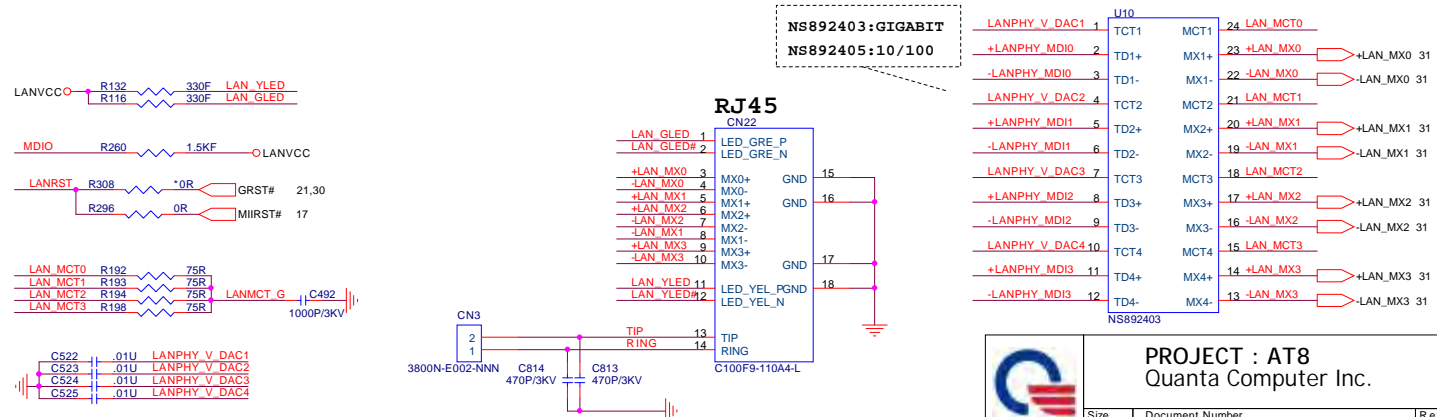
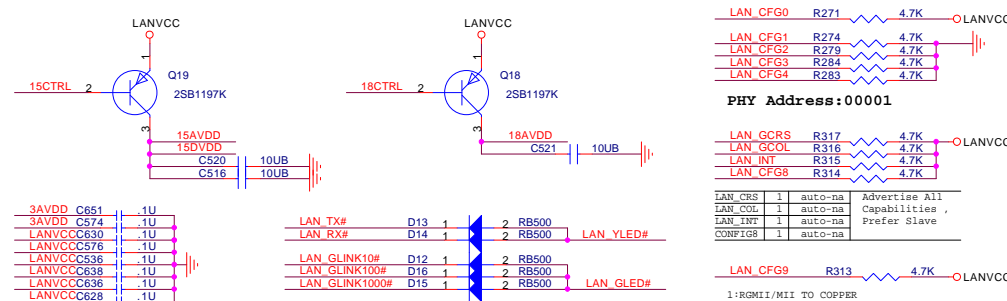
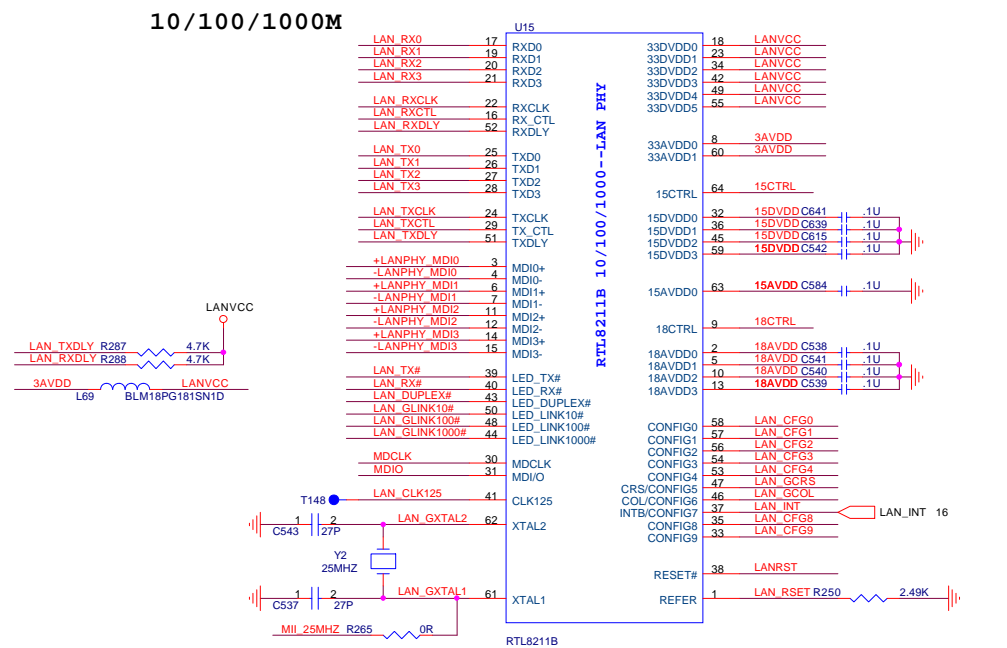
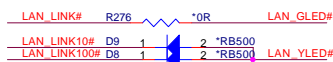
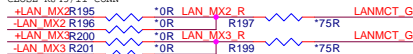


LAN_COL	1		RTL8201BL LED
	0	Default	RTL8201CL LED
LAN_RXER_R	1		Fiber Mode
	0	Default	UTP Mode
LAN_CRS	0	Default	Ensure operating at normal mode

CLOSE TRANSFORMER

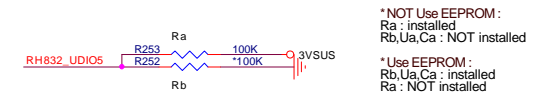
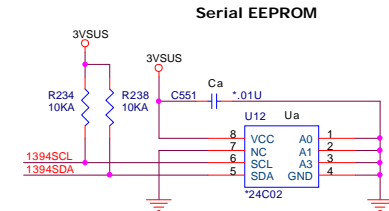
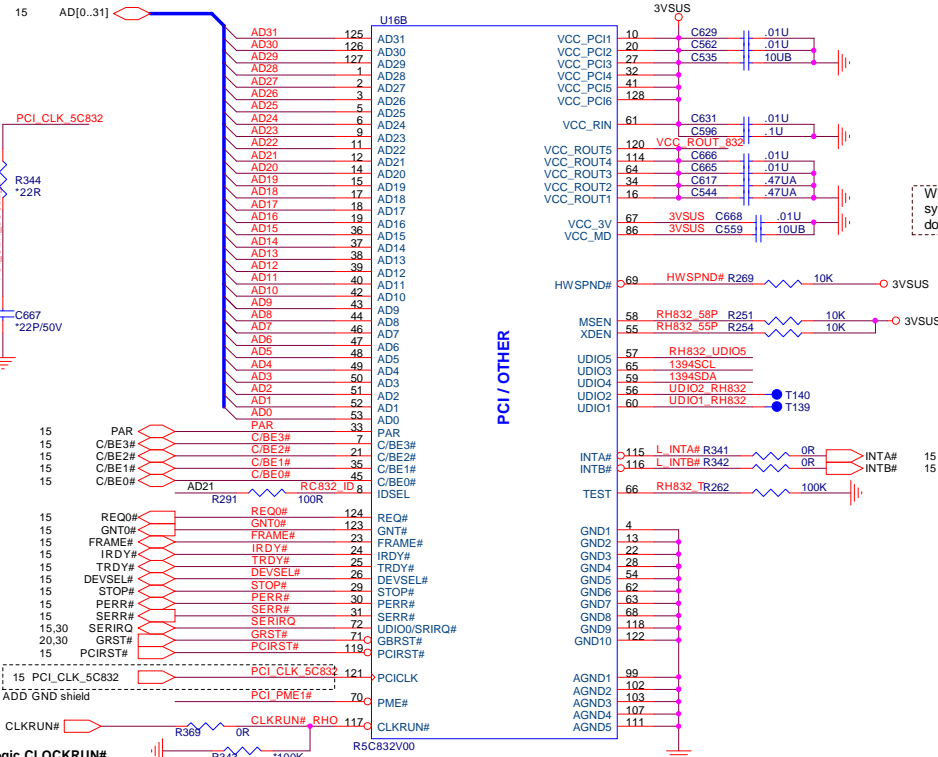


CLOSE RJ45/11 CONT



PROJECT : AT8
Quanta Computer Inc.

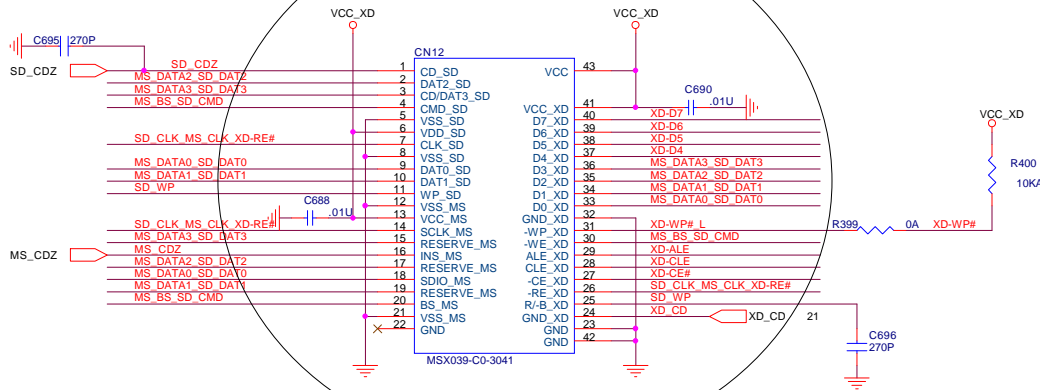
Size Custom	Document Number LAN PHY RTL8211B/8201CL,RJ45	Re 1/
Date: Tuesday, February 07, 2006		Sheet 20 of 38



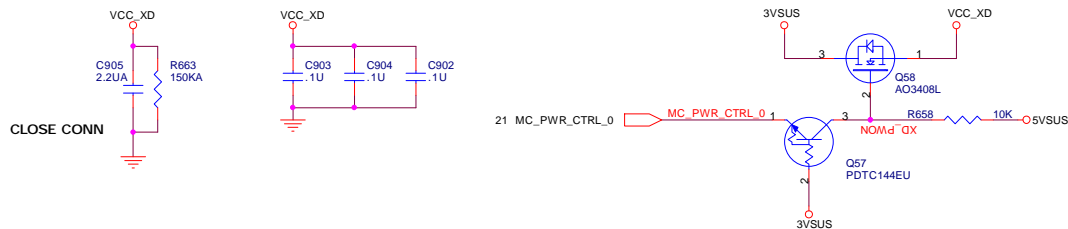
4 IN1 CARD READER

XD, MMC/SD, MS/MSP

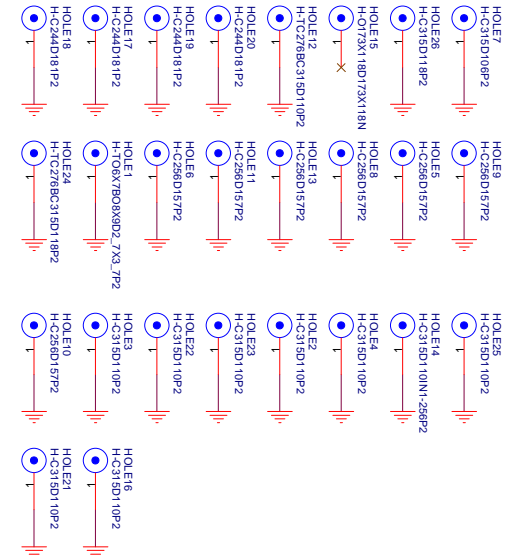
CHECK CONN.



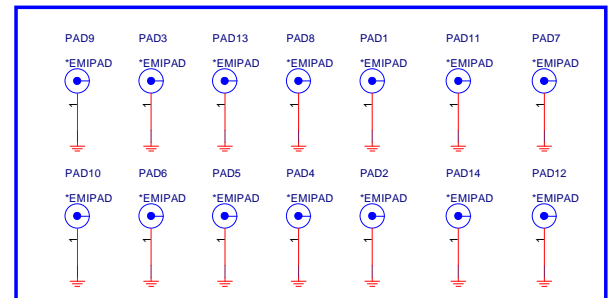
21	MDIO03	MDIO03	R398	56R	SD_WP
21	MDIO17	MDIO17	R409	56R	XD-D7
21	MDIO16	MDIO16	R408	56R	XD-D6
21	MDIO15	MDIO15	R407	56R	XD-D5
21	MDIO14	MDIO14	R406	56R	XD-D4
21	MDIO13	MDIO13	R405	56R	MS_DATA3_SD_DATA3
21	MDIO12	MDIO12	R404	56R	MS_DATA2_SD_DATA2
21	MDIO11	MDIO11	R403	56R	MS_DATA1_SD_DATA1
21	MDIO10	MDIO10	R402	56R	MS_DATA0_SD_DATA0
21	MDIO08	MDIO08	R397	56R	MS_BS_SD_CMD
21	MDIO05	MDIO05	R401	56R	XD-WP#
21	MDIO19	MDIO19	R396	56R	XD-ALE
21	MDIO18	MDIO18	R395	56R	XD-CLE
21	MDIO02	MDIO02	R394	56R	XD-CE#
21	MDIO09	MDIO09	R393	56R	SD_CLK_MS_CLK_XD-RE#



SCREW HOLE



EMI PAD

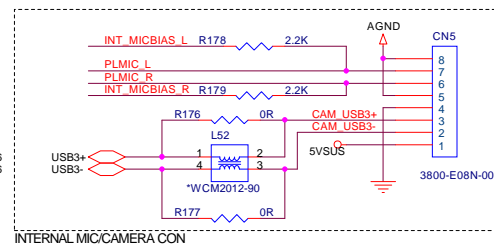
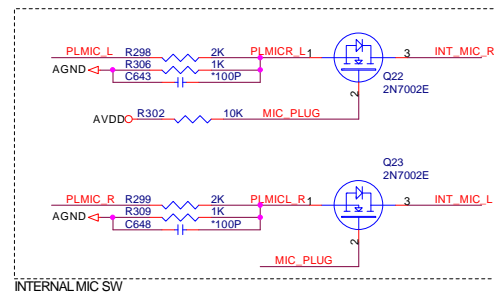
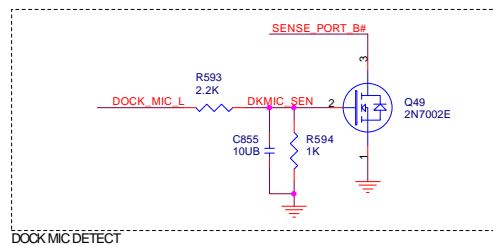
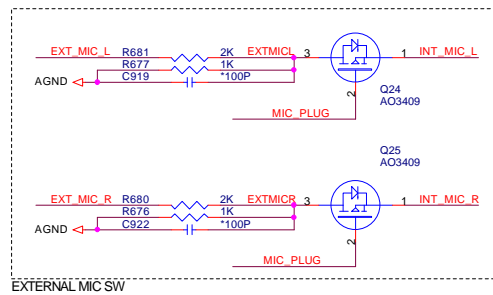
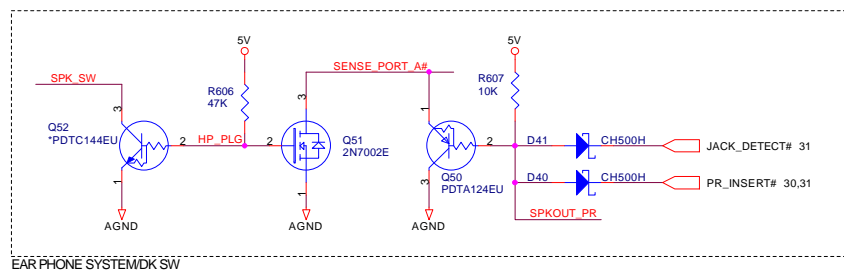
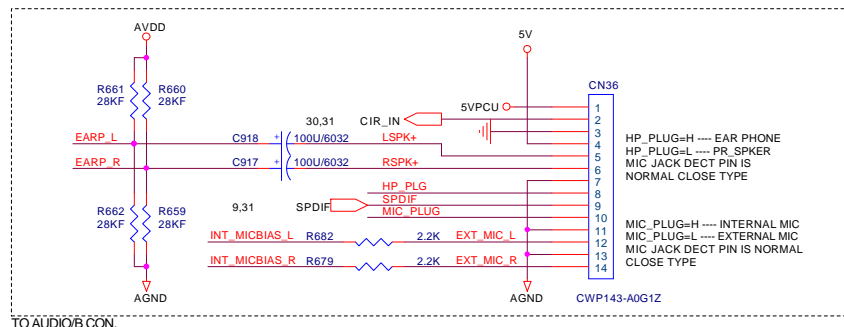
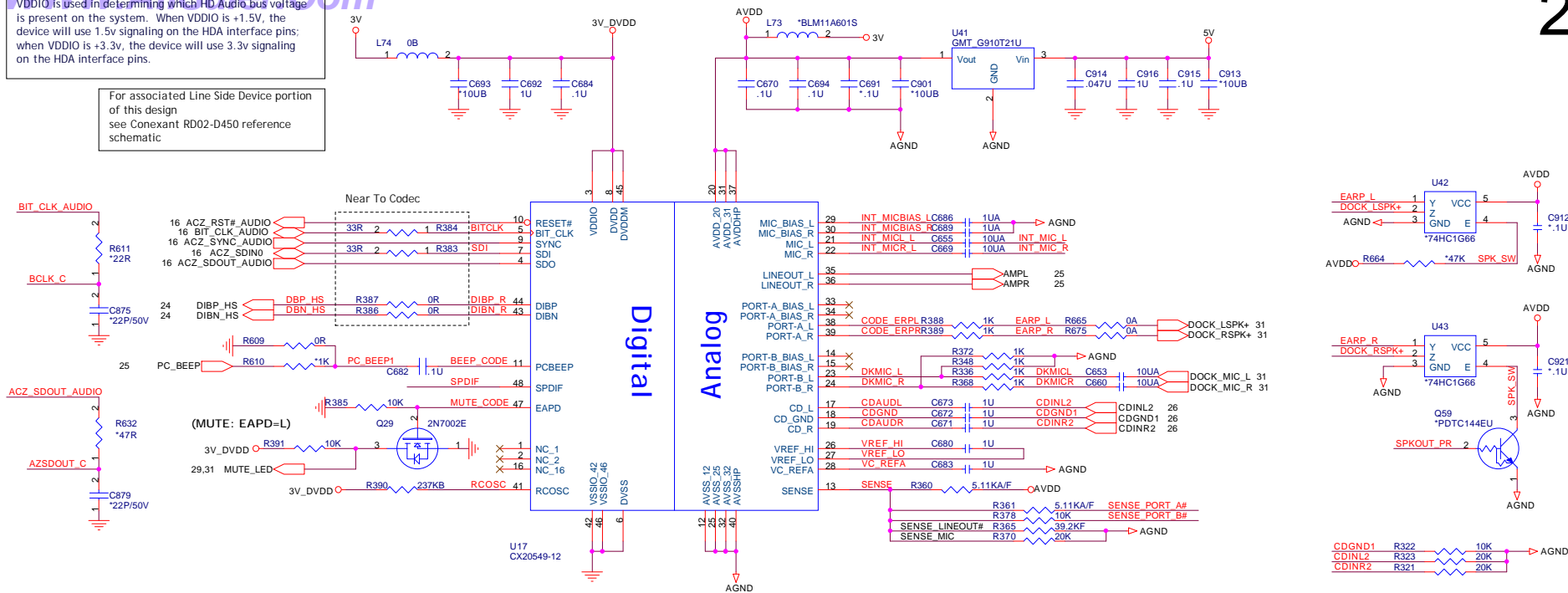


PROJECT : AT8
Quanta Computer Inc.

Size Custom	Document Number CARD READER CONN	Rev 1A
Date: Thursday, December 29, 2005	Sheet 22 of 38	

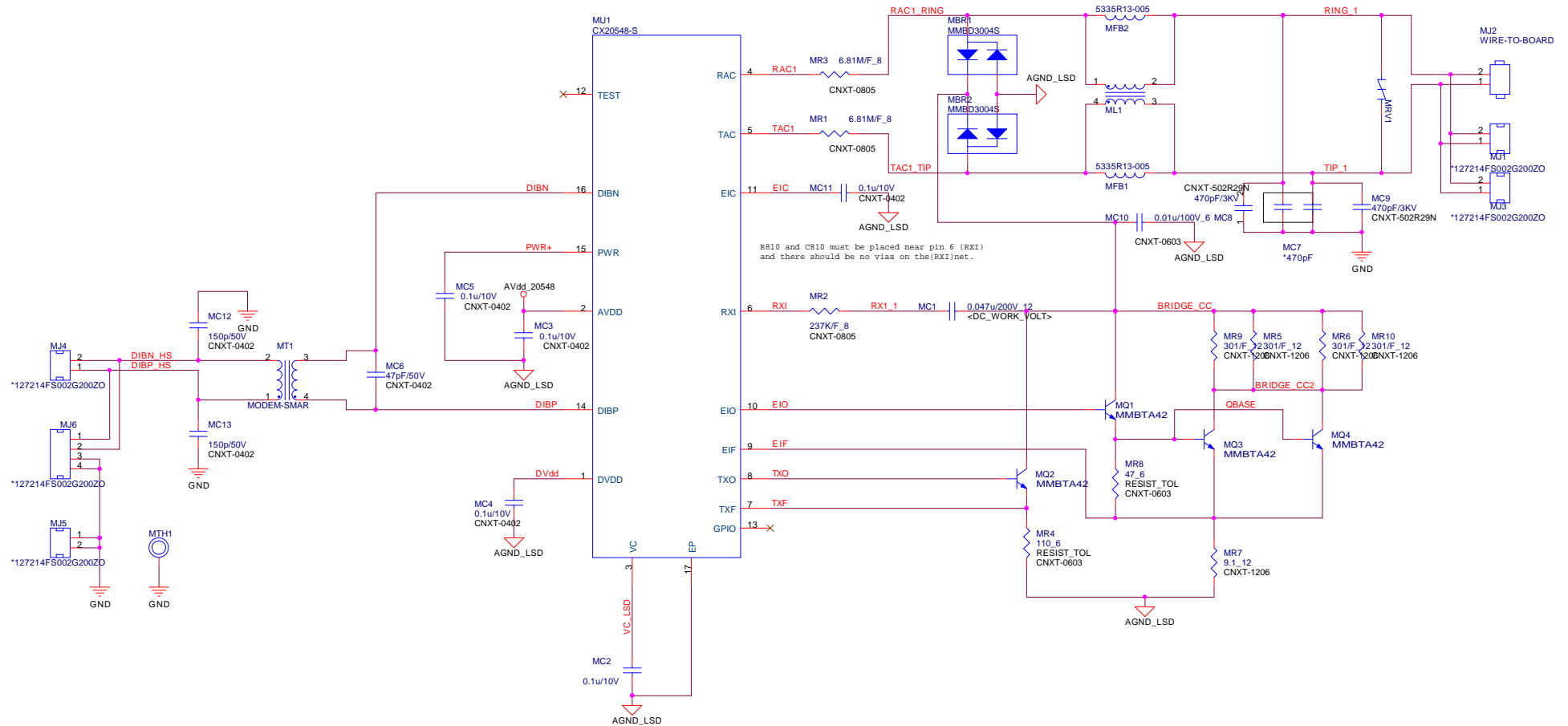
VDDIO is used in determining which HD Audio bus voltage is present on the system. When VDDIO is +1.5v, the device will use 1.5v signaling on the HDA interface pins; when VDDIO is +3.3v, the device will use 3.3v signaling on the HDA interface pins.


For associated Line Side Device portion of this design see Conexant RD02-D450 reference schematic

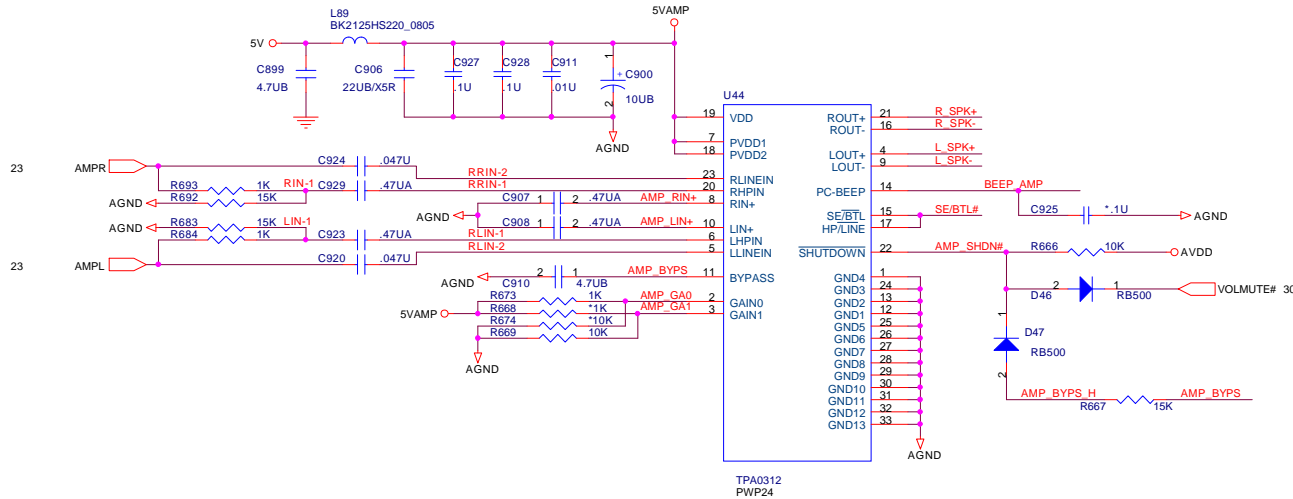


Revision History		
REV	Description	Date
0	Initial Release	April 26, 2005

23 DIBN_HS
23 DIBP_HS

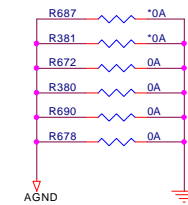


 NB5/RD2/HW1	PROJECT : AT8 Quanta Computer Inc.	
	Size Custom	Document Number MODEM (DAA)
	Date: Thursday, December 29, 2005	Sheet 24 of 38

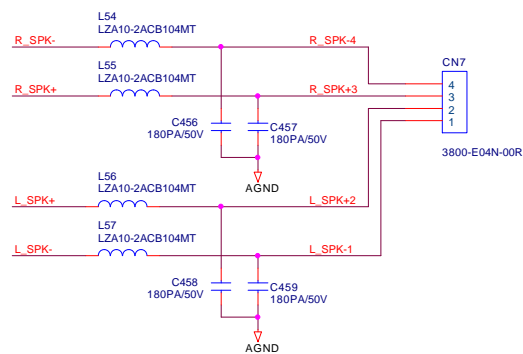


0312 Gain Table

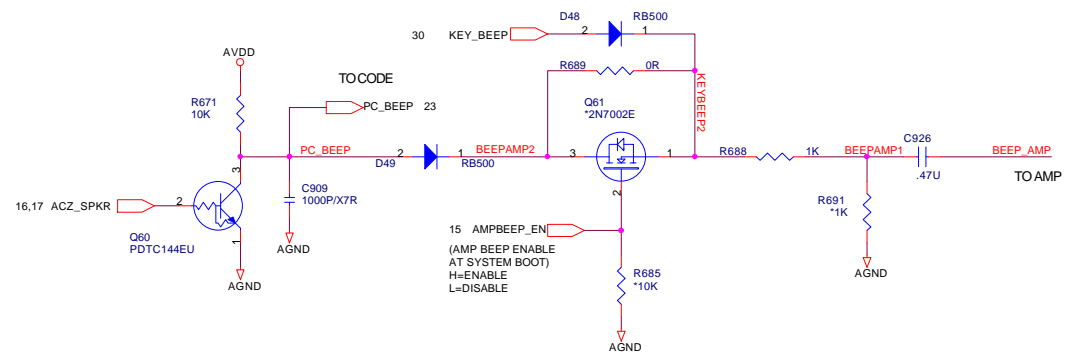
GAIN0	GAIN1	SE/BTL	AV(INV)
0	0	0	6dB
0	1	0	10dB
1	0	0	15.6dB
1	1	0	21.6dB
x	x	1	4.1dB



INT. SPEAKER

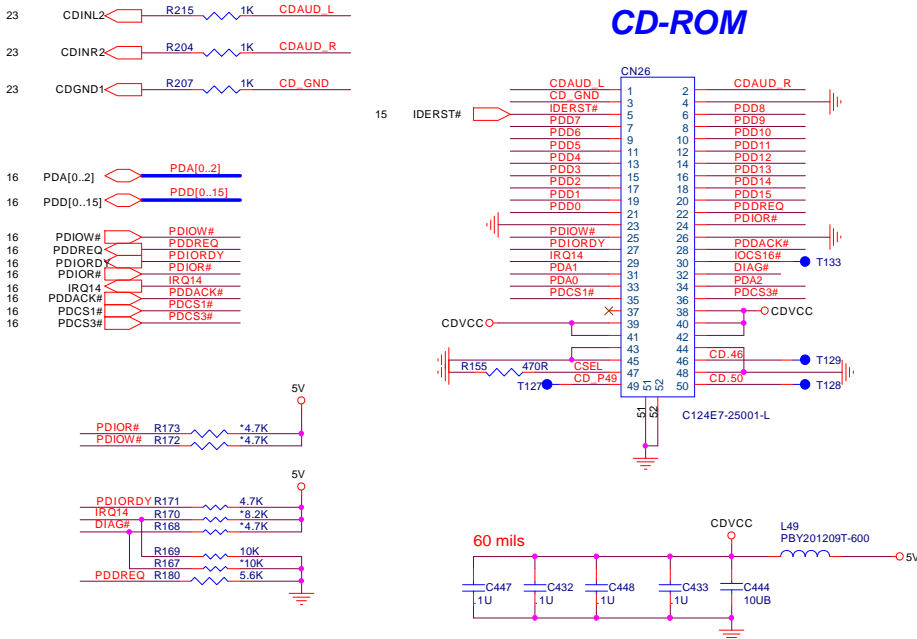


PCSPK BEEP

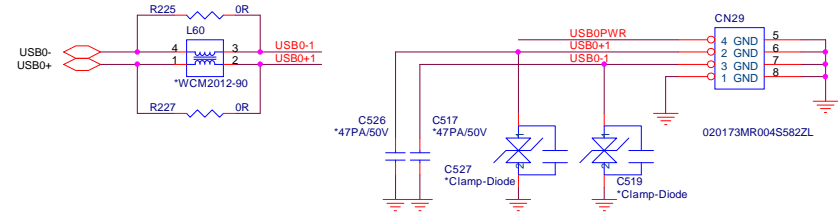
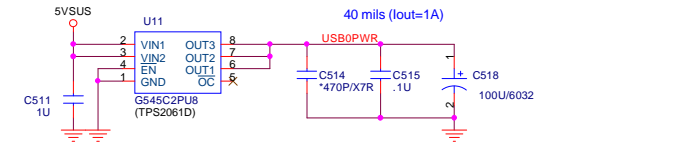


PROJECT : AT8
Quantia Computer Inc.

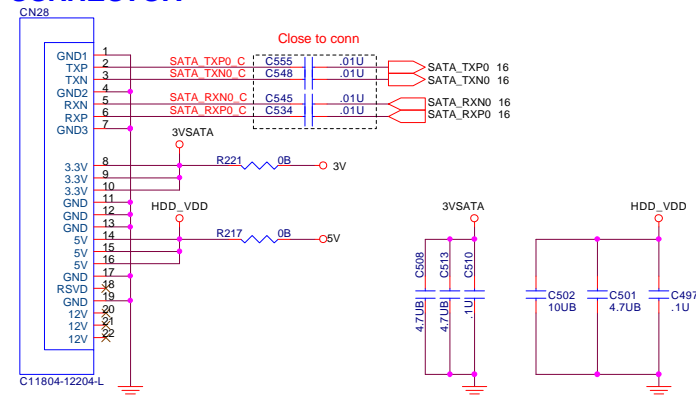
Size Custom	Document Number JACK_AMP_TPA0312	Rev 1A
Date: Thursday, December 29, 2005	Sheet 25 of 38	



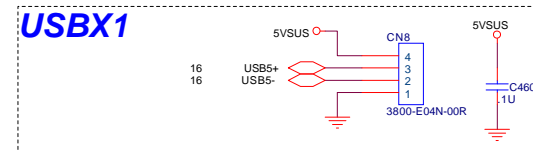
USBX2




SATA 1 CONNECTOR



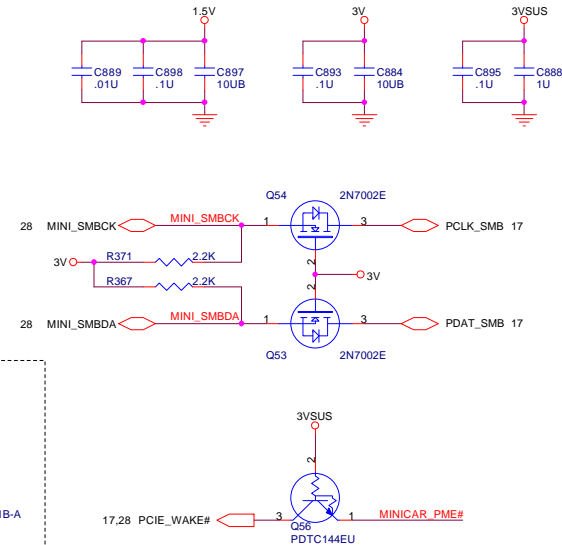
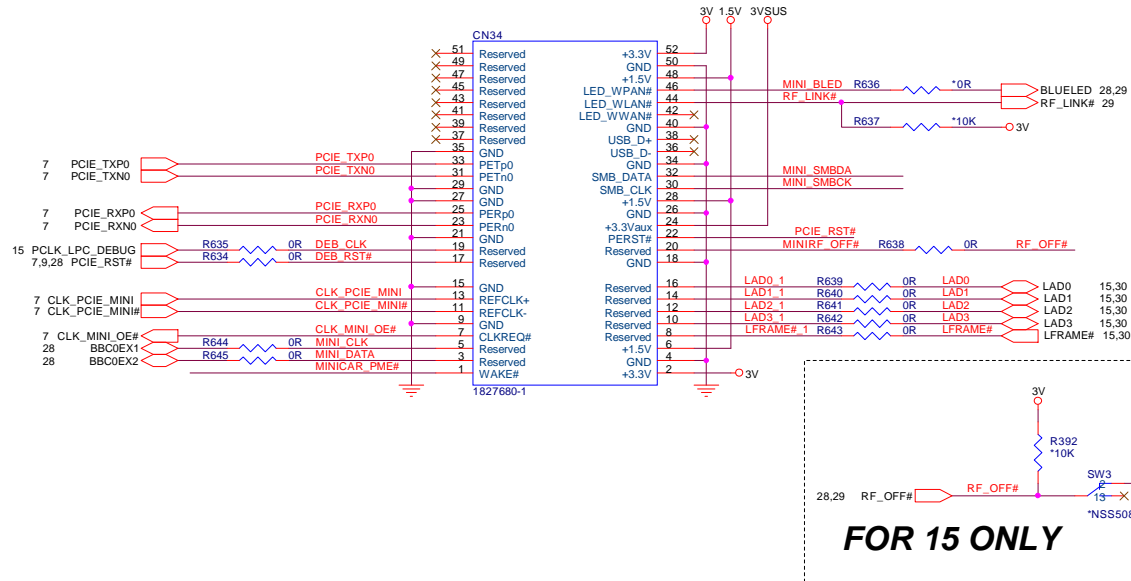
USBX1



TO AD/B

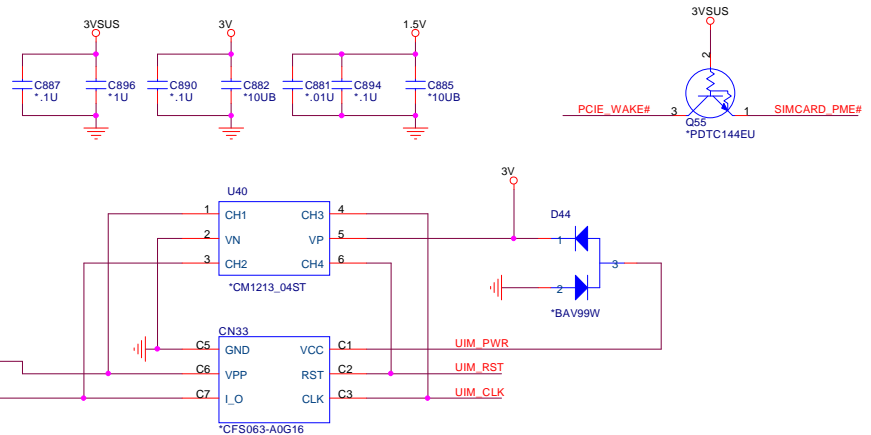
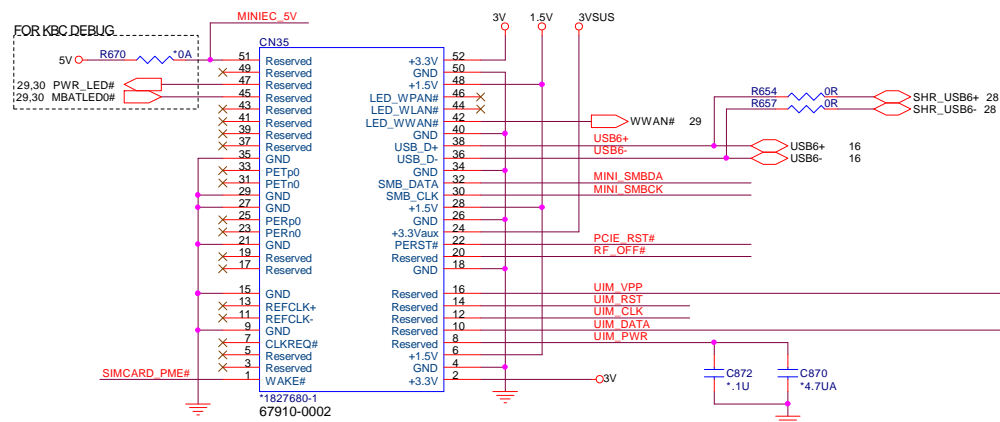
	PROJECT : AT8		
	Quanta Computer Inc.		
	Size Custom	Document Number	Rev 1A
	NB5/RD2/HW1	SATA HDD, CD-ROM, USBX2	
Date: Thursday, December 29, 2005 Sheet 26 of 38			


Mini PCI-E Card 1 WLAN



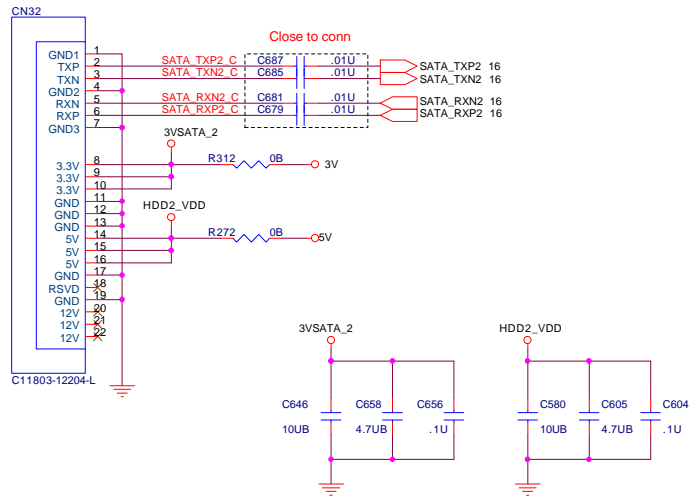
Mini PCI-E Card 2 WWAN(W/SIM)

FOR 15 ONLY



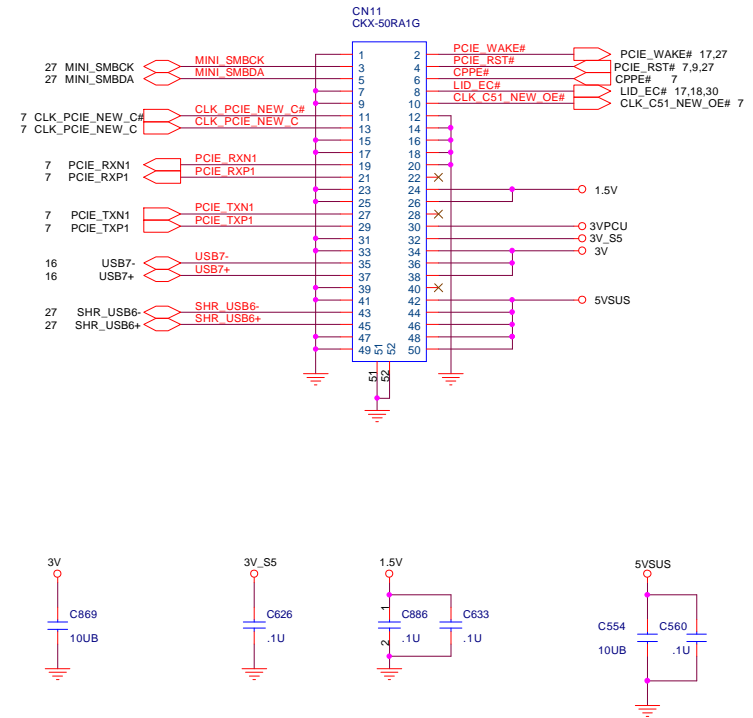
	PROJECT : AT8 Quanta Computer Inc.		
	Size Custom	Document Number MINI CARD	Rev 1A
	Date: Thursday, December 29, 2005 Sheet 27 of 38		
	NB5/RD2/HW1		

**SATA_2 CONNECTOR
For 17"W Second HDD**

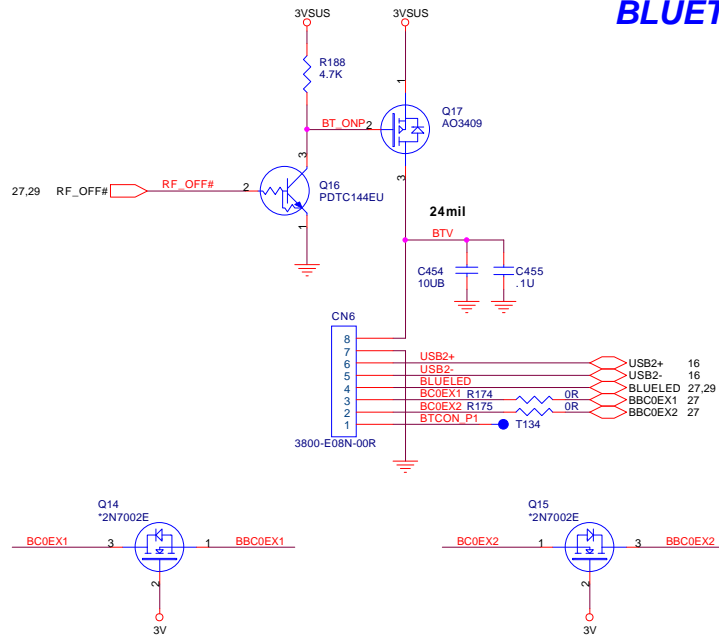



NEWCARD

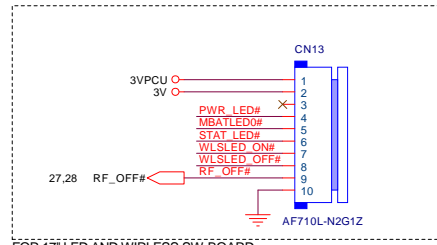
28



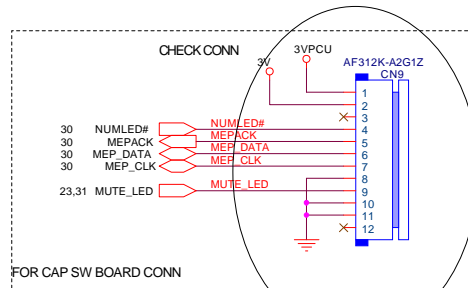
BLUETOOTH



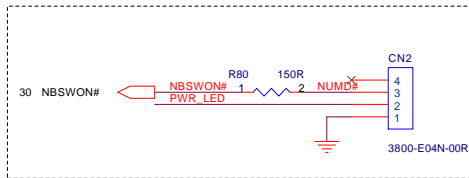
	PROJECT : AT8 Quanta Computer Inc.		
	Size Custom	Document Number NEW CARD/BT	Rev 1A
Date: Thursday, December 29, 2005 Sheet 28 of 38			



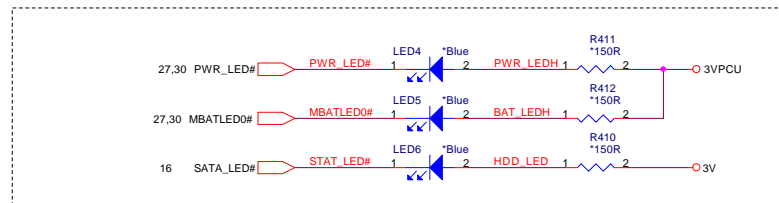
FOR 17" LED AND WIRLESS SW BOARD



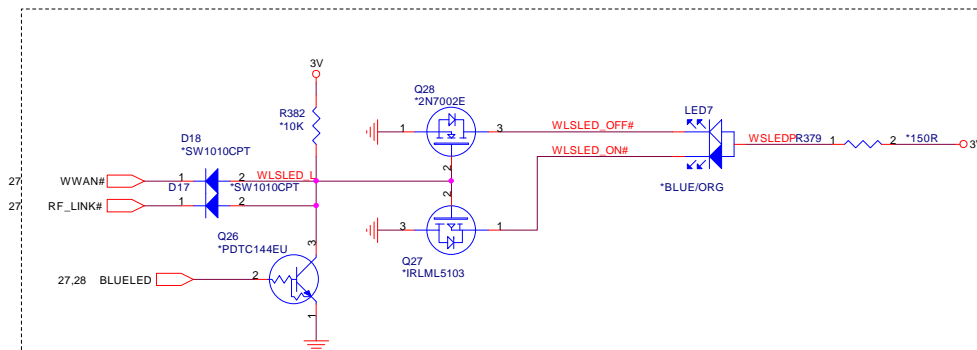
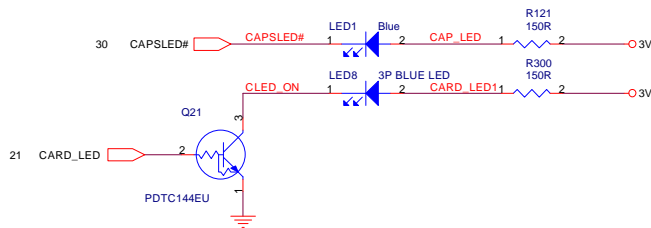
FOR CAP SW BOARD CONN



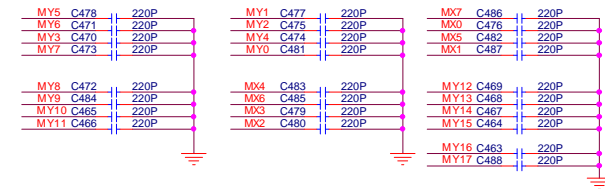
FOR POWER ON SW BOARD



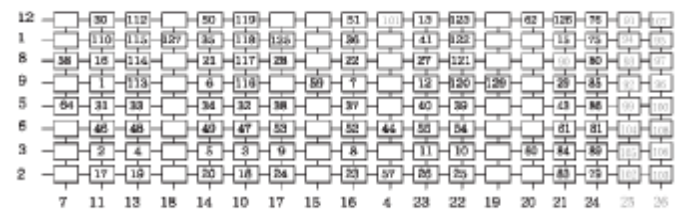
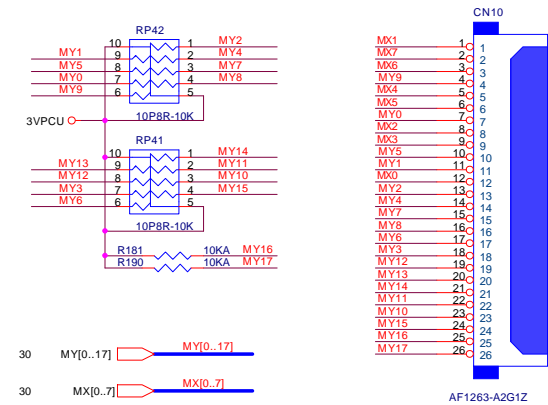
FOR 15.4" LED



FOR WIRELESS LED



KEYBOARD PULL-UP



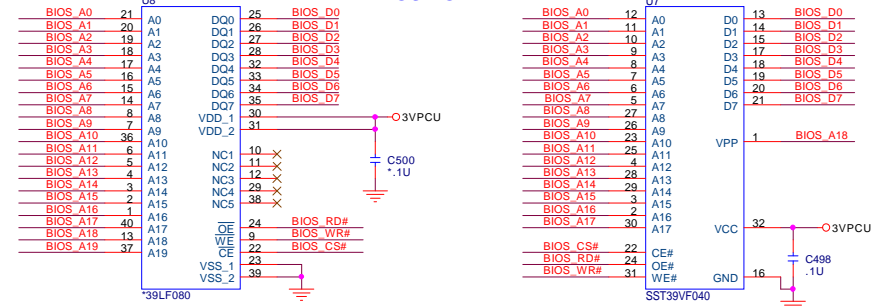
PROJECT : AT8
Quanta Computer Inc.

Size Custom	Document Number LED/KEYBOARD/TP	Rev 1A
Date: Thursday, December 29, 2005 Sheet 29 of 38		



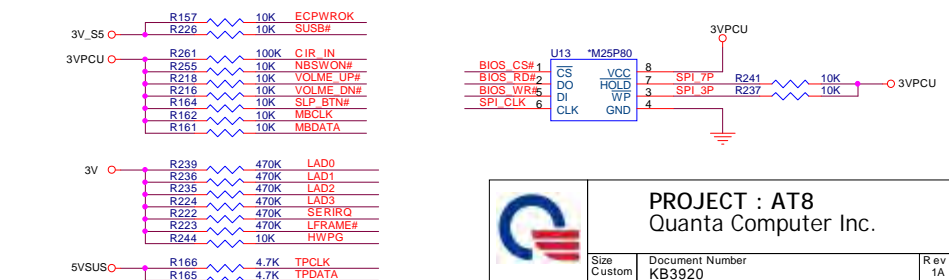
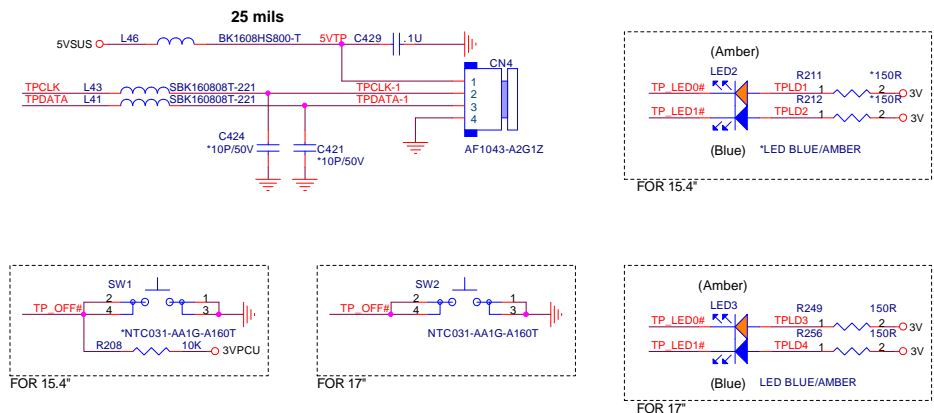
30

BIOS ROM



ROM CHIP P/N:AKE34ZAPK01

TOUCH PAD CONNECTOR



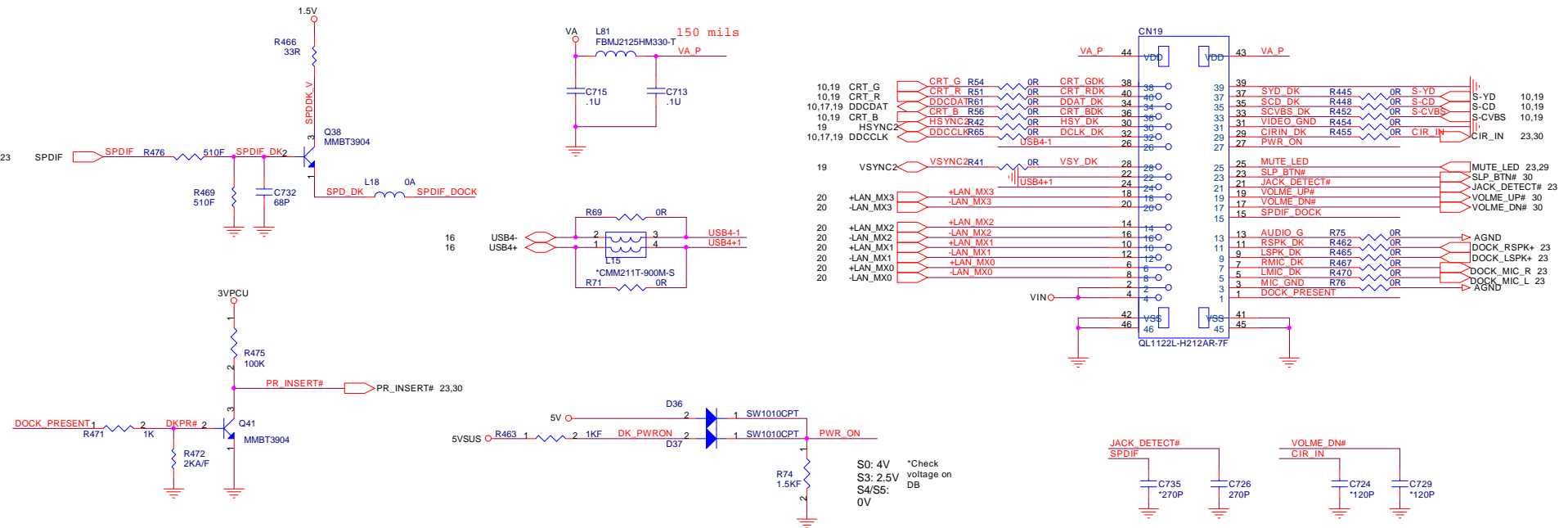
PROJECT : AT8
Quanta Computer Inc.



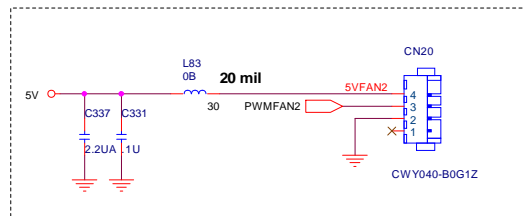
NB5/RD2/HW

Size Custom	Document Number KB3920	Rev 1A
Date: Thursday, December 29, 2005		Sheet 30 of 38

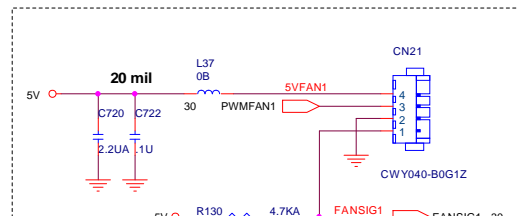
CABLE DOCK



FAN

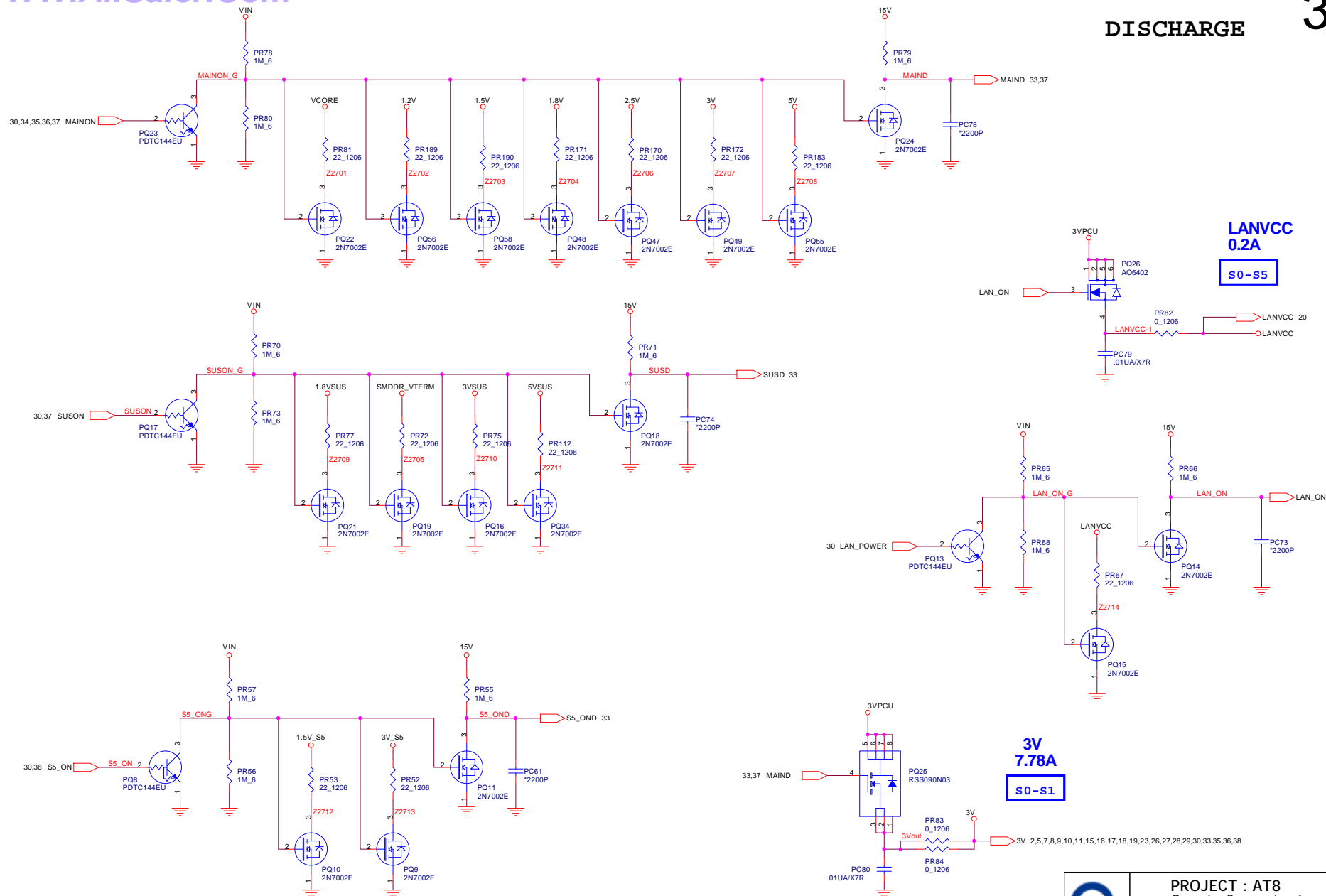


FAN2 PWM CONNECTOR



FAN1 PWM CONNECTOR

	PROJECT : AT8 Quanta Computer Inc.		
	Size Custom	Document Number CABLE DOCKING	Rev 1A
	Date: Thursday, December 29, 2005 Sheet 31 of 38		



DC-DC (MAX8734A)

HW RESET USAGE

3VPCU
0.06A
Always
OCP=12A


5VPCU
0.01A
Always
OCP=12A

3V_S5
0.2A
S0-S5

3VSUS
1.6A
S0-S3

5V
4.69A
S0-S1

5VSUS
5A
S0-S3

		
PROJECT : AT8 Quanta Computer Inc.		
Size Custom	Document Number MAX8734A (3V/5V/15V)	Rev 1A
Date: Thursday, December 29, 2005	Sheet 33	of 38

Adapter

19V/3.4A/65W
4.75A/90W

MAX8724A

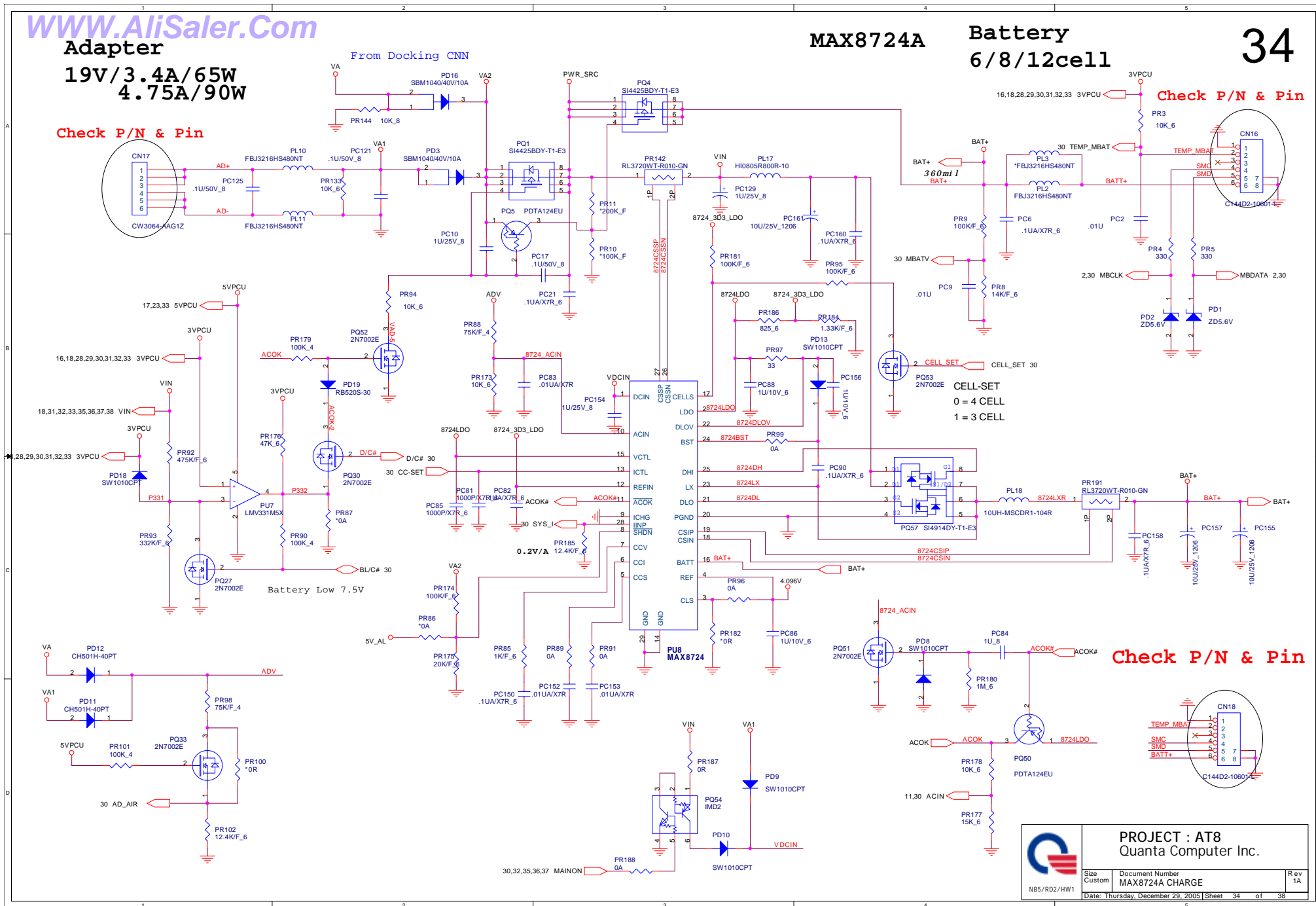
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6/8/12cell

34

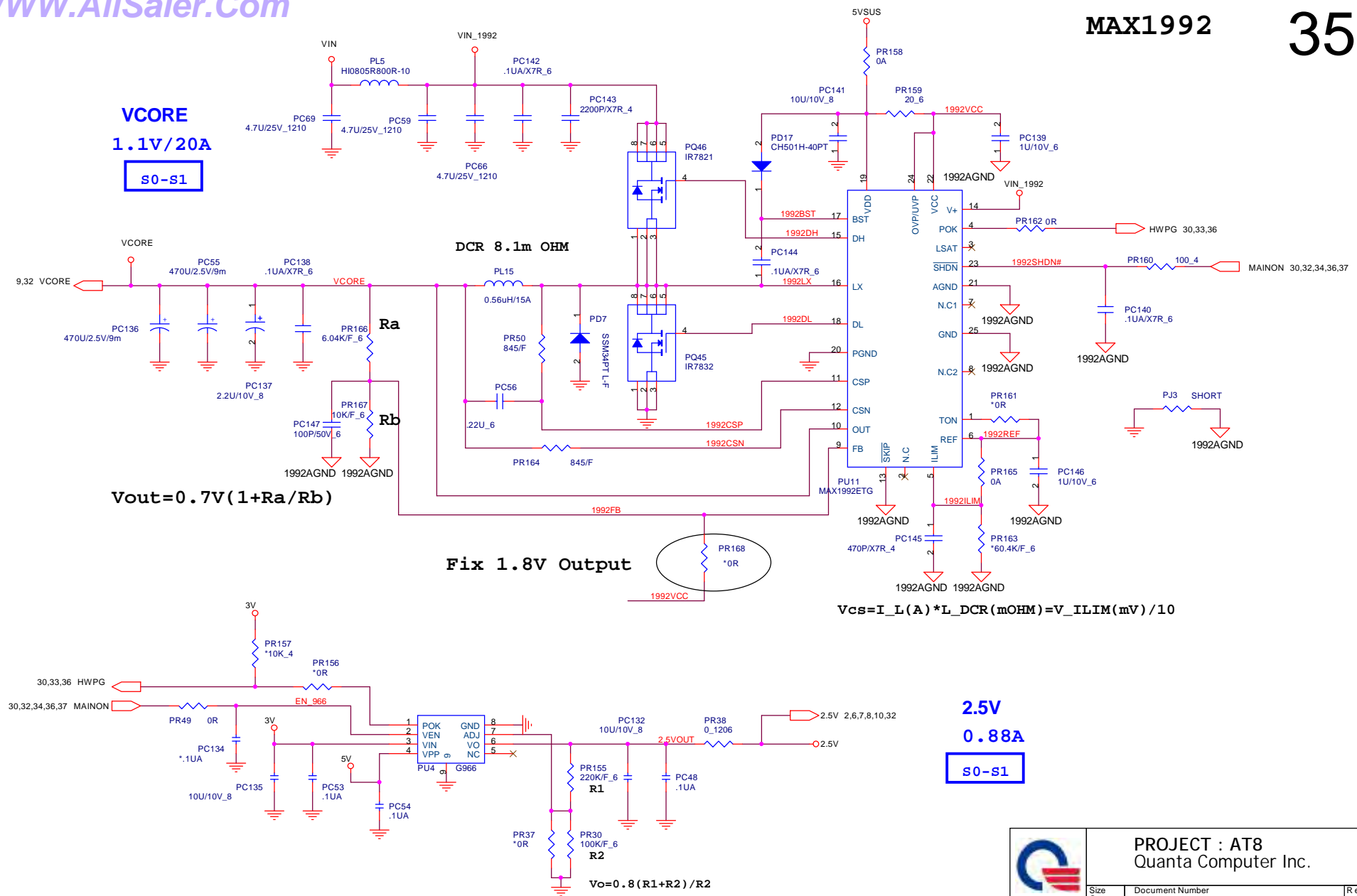
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Check P/N & Pin



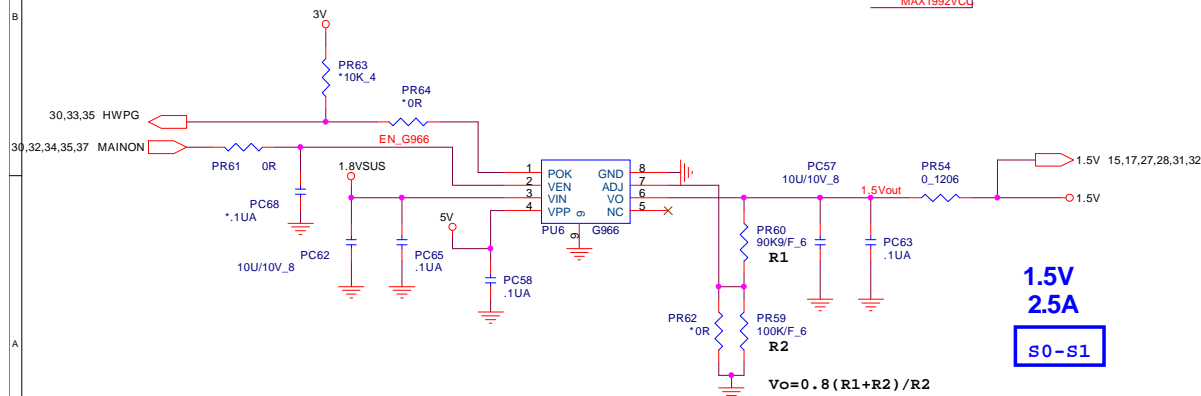
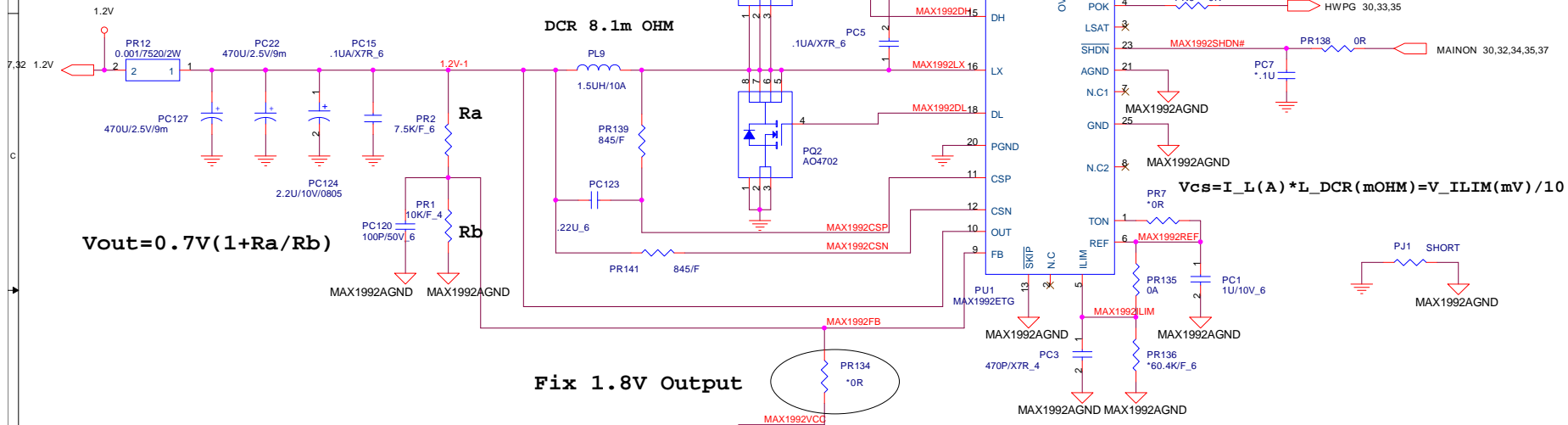
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1.2V

S0-S1

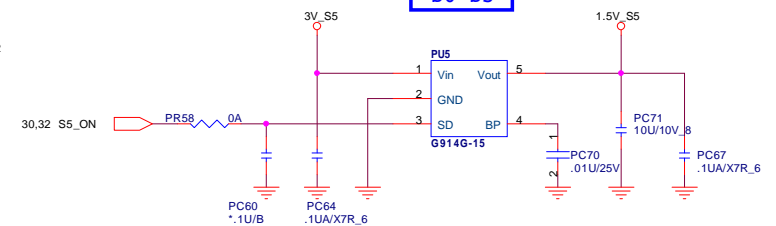
9.35A



1.5V_S5

0.2A

S0-S5



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Quanta Computer Inc.

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